RG22Regulations



GEETHANJALI INSTITUTE OF SCIENCE & TECHNOLOGY: NELLORE (AUTONOMOUS)

NELLORE-524317(A.P) INDIA

B.TECH

IN

ELECTRONICS & COMMUNICATION ENGINEERING

COURSE STRUCTURE AND SYLLABI

UNDER

RG 22 REGULATIONS





GEETHANJALI INSTITUTE OF SCIENCE & TECHNOLOGY: NELLORE

AUTONOMOUS

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

(ACCREDITATED BY NBA)

DEPARTMENT VISION

Achieving academic excellence in Electronics and Communication Engineering by shaping nextgeneration technocrats keeping pace with socio-economic needs.

DEPARTMENT MISSION

M1: Adopting outcome oriented teaching -learning processes to provide comprehensive knowledge in the application of Electronics and Communication Engineering principles.

M2: Striving for implementation of advanced technology to cater to industrial demands and societal concerns.

M3: Producing highly skilled and responsible professionals with robust ethical values.

M4: Integrating technical capabilities, life skills and entrepreneurship abilities to produce dynamic contributors to social advancement.

Program Educational Objectives (PEOs)

PEO-1: Demonstrating a deep passion for continuous learning through technical expertise for a promising career.

PEO-2: Exhibiting a strong commitment to serving the society with adherence to professional ethics.

PEO-3: Managing resources efficiently as competent engineers through effective social interaction.

PEO-4: Engaging in advanced learning and contributing to technological innovations.



PO1	Engineering knowledge: Apply the knowledge of mathematics, science, engineering
	fundamentals, and an engineering specialization to the solution of complex engineering
	problems.
PO2	Problem analysis: Identify, formulate, review research literature, and analyze complex
	engineering problems reaching substantiated conclusions using first principles of
	mathematics, natural sciences, and engineering sciences.
PO3	Design/development of solutions: Design solutions for complex engineering problems and
	design system components or processes that meet the specified needs with appropriate
	consideration for the public health and safety, and the cultural, societal, and environmental
	considerations.
PO4	Conduct investigations of complex problems: Use research-based knowledge and
	research methods including design of experiments, analysis and interpretation of data, and
	synthesis of the information to provide valid conclusions.
PO5	Modern tool usage: Create, select, and apply appropriate techniques, resources, and
	modern engineering and IT tools including prediction and modelling to complex
	engineering activities with an understanding of the limitations.
PO6	The engineer and society: Apply reasoning informed by the contextual knowledge to
	assess societal, health, safety, legal and cultural issues and the consequent responsibilities
	relevant to the professional engineering practice.
PO7	Environment and sustainability: Understand the impact of the professional engineering
	solutions in societal and environmental contexts, and demonstrate the knowledge of, and
	need for sustainable development
PO8	Ethics: Apply ethical principles and commit to professional ethics and responsibilities and
DOG	norms of the engineering practice.
PO9	Individual and team work: Function effectively as an individual, and as a member or
DO10	leader in diverse teams, and in multidisciplinary settings.
PO10	Communication: Communicate effectively on complex engineering activities with the
	engineering community and with society at large, such as, being able to comprehend and
	write effective reports and design documentation, make effective presentations, and give
DO11	and receive clear instructions.
PO11	Project management and finance : Demonstrate knowledge and understanding of the
	engineering and management principles and apply these to one's own work, as a member
DO12	and leader in a team, to manage projects and in multidisciplinary environments.
PO12	Life-long learning: Recognize the need for, and have the preparation and ability to engage
	in independent and life-long learning in the broadest context of technological change.

Program Specific Outcomes

PSO1 Design and develop electronic circuits and communication systems, applying the principlesof signal, image processing, VLSI, Embedded and wireless applications relevant to industry and society.

PSO2 Adopting software tools like Matlab, Xilinx, Microwind, NS-2 to develop intelligent systems to offer customized solutions.



	Semester - 5 (Theory-6, Lab-2, MC-1)							
Sl.	Category	Course	Course Title	Hours per week		Credits		
No.		Code		L	Т	P	С	
1	PCC	22A0420T	Digital System Design through Verilog	3	0	0	3	
2	PCC	22A0215T	Control Systems Engineering	3 0 0			3	
3	PCC	22A0421T	Antennas & MicrowaveEngineering	3	0	0	3	
4	PEC		Professional Elective-I	3	0	0	3	
5	OEC		Open Elective-I	3	0	0	3	
6	PCC (Lab)	22A0426P	Digital System Design through Verilog Lab	0	0	3	1.5	
7	PCC (Lab)	22A0429P	Antennas & MicrowaveEngineering Lab	0	0	3	1.5	
8	SC	22A0029P	Skill Advanced Course: Soft Skills	1	0	2	2	
9	MC	22A0526	Mandatory Course: Design Thinking and Innovation	2	0	0	0	
		22A0433	Evaluation of Community Service Project	0	0	0	1.5	
	Total credits 2						21.5	

S. No.	Course Code	Name of the Professional Elective-I	
1	22A0422T	Data Communication & Networks	
2	22A0423T	Information Theory and Coding	
3	22A0424T	Industrial Electronics	
4	22A0425T	Computer Architecture & Organization	

S. No.	Course Code	Name of the Open Elective-I
1	22A0512T	Data base Management Systems
2	22A0258T	Applications of Power Electronics To Power Systems
3	22A0334Tc	Fundamentals of Drone Technology
4	22A0149T	Building Materials

Category	Credits
Professional Core Courses (PCC)	12
Professional Elective Courses (PEC)	3
Open Elective Courses (OEC)	3
Skill Advanced Course (SC)	2
Summer Internship	1.5
Total	21.5

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	DIGIT		E – RG 22 Regul DESIGN THRO	ation UGH VERILOG	
Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0420T	3:0:0	3	CIE:30 SEE:70	3 Hours	PCC
Course Object	ives:		I		I
• To designi HDL	ng digital cir			eling of digital circ	
• The Studer several dig	nts aims to praital circuits.	actical experien	ce by designing,	to standard cell libra modelling, implemer	nting and verifying
			anding of the dir HDL programs us	fferent technologies sing provided	related to HDLs,
	1	Syllabu	1 0	01	Total Hours: 48
τ	J nit –I::Intr o	v	asics of Verilog	HDL	10 Hrs
Introduction t	to Verilog H	IDL: Verilog	as HDL, Levels	of Design Descript	ion, Concurrency,
Simulation and	Synthesis, Pr	ogramming La	nguage Interface,	Module.	
Language Co	onstructs an	d Convention	s: Introduction,	Keywords, Identifie	ers, White Space,
Characters, Co	omments, Nu	mbers, Strings	s, Logic Values,	Data Types, Sca	lars and Vectors,
Operators.					
	Unit –II:: G	ate Level Mod	eling and Data f	low	10 Hrs
Gate Primitives Modeling at	, Gate Delay, Dataflow L	Strengths and evel: Introduc	Contention Resolu	Primitives, Design o ution, Net Types. Assignment Struc	
	0	-III:: Behavio	· •		10 Hrs
construct, Assig Non Blocking	odeling: Int gnments with Assignments, peat' Constru	roduction, Ope Delays, 'Wait The 'Case' Sta uct, for loop, '	erations and Ass 'Construct, Desi tement, 'If' and 'if	ignments, 'Initial' C gn at Behavioral Le E-Else' Constructs, 'A nstruct, 'While Loo	vel, Blocking and Assign- De-Assign'
1		IV:: Switch Le	evel Modeling		9 Hrs
	Modeling: B vitch Primitiv	asic Transistor	Switches, CMOS	S Switches, Bidirect and delays, Switch	ional Gates, Time
Unit –V:: S	ystem Tasks,	Functions & (Compiler Directi	ves & Sequential	9 Hrs
		Circuit Desc	•		
System Tasks a	nd Functions	, User Defined	Primitives, Comp	eters, Path Delays, M iler directives. ack Model, Capaciti	
	anabhan, B B	ala Tripura Sun	dari, Design throu	ugh Verilog HDL, W	7 iley 2009.

2. 2. Verilog HDL - Samir Palnitkar, 2nd Edition, Pearson Education, 2009.

References:

- Fundamentals of Digital Logic with Verilog Design Stephen Brown, Zvonkoc Vranesic, TMH, 2nd Edition.
- 2. Zainalabdien Navabi, Verliog Digital System Design, TMH, 2nd Edition.
- Advanced Digital Logic Design using Verilog, State Machines & Synthesis for FPGA -Sunggu Lee, Cengage Learning, 2012.
- 4. Advanced Digital Design with Verilog HDL Michel D. Ciletti, PHI, 2009.

Course Outcomes:

After the completion of the course students will able to:

CO1: Describe Verilog HDL Design Digital circuits

CO2: Write behavior model of digital circuits

CO3: Write RTL models of digital circuits

CO4: Describe standard Cell Libraries and FPGAs

CO5: Synthesize RTL models to standard cell libraries and FPGAs

CO6: Implement RTL models on FPGAs and testing and verification

CONTROL SYSTEMS ENGINEERING

Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0215T	3:0:0	3	CIE:30	3 Hours	PCC
			SEE:70		

Course Objectives:

- Merits and demerits of open loop and closed loop systems; the effect of feedback
- The use of block diagram algebra and Mason's gain formula to find the overall transfer function
- Transient and steady state response, time domain specifications and the concept of Root loci
- Frequency domain specifications, Bode diagrams and Nyquist plots

State space modelling of Control system				
Unit-I	Unit-I Concept of Control System 10 Hrs			
Classification of control	I systems - Open loop and closed loop control system	ms, Differences,		
Examples of control syste	ems- Effects of feedback, Feedback Characteristics. Math	ematical models		
Principle of operation of	DC and AC Servo motor, Transfer function of DC se	rvo motor - AC		
servo motor, Synchros.				
Transfer Function Repr	resentation: Block diagram algebra, Determining the Tra	nsfer function		
from Block Diagrams, Sig	gnal flow graphs(SFG) - Reduction using Mason's gain for	ormula Transfer		
function of SFG's.				
Unit –II	Time Response Analysis	10 Hrs		
Step Response - Impulse	Response - Time response of first order systems - Chara	cteristic Equation		
of Feedback control sy	stems, Transient response of second order systems	- Time domain		
specifications – Steady	state response - Steady state errors and error const	ants, P, PI, PID		
Controllers.				
Unit –III	Stability Analysis in Time Domain	10 Hrs		
A) Stability Analysis in S-Domain: The concept of stability – Routh-Hurwitz's stability criterion				
- qualitative stability and	conditional stability - Limitations of Routh-Hurwitz's st	ability		
-	ue: Concept of root locus - Construction of root locus	, Effects of adding		
and zeros to G(s) H(s) on the root loci.				
Unit –IV	Frequency Response Analysis	9 Hrs		
Introduction, Frequency domain specifications-Bode diagrams-Determination of Frequency domain				
specifications and transfer function from the Bode Diagram-Stability Analysis from Bode Plots.				
Polar Plots-Nyquist Plots	Polar Plots-Nyquist Plots- Phase margin and Gain margin-Stability Analysis.			
Compensation techniques	s - Lag, Lead, Lag-Lead Compensator design in frequenc	y Domain.		

State Space Analysis of Continuous Systems 9 Hrs Unit-V

Concepts of state, state variables and state model, state models - differential equations & Transfer function models - Block diagrams. Diagonalization, Transfer function from state model, Solving the Time invariant state Equations- State Transition Matrix and it's Properties. System response through State Space models. The concepts of controllability and observability, Duality between controllability and observability.

Text Books:

- 1. Modern Control Engineering by Katsuhiko Ogata, Prentice Hall of India Pvt. Ltd., 5th edition, 2010.
- 2. Control Systems Engineering by I. J. Nagrath and M. Gopal, New Age International (P) Limited Publishers, 5th edition, 2007.



References:

- 1. Control Systems Principles & Design by M.Gopal, 4th Edition, Mc Graw Hill Education, 2012.
- 2. Automatic Control Systems by B. C. Kuo and Farid Golnaraghi, John wiley and sons, 8th edition, 2003.
- 3. Feedback and Control Systems, Joseph J Distefano III, Allen R Stubberud& Ivan J Williams, 2nd Edition, Schaum's outlines, Mc Graw Hill Education, 2013.
- 4. Control System Design by Graham C. Goodwin, Stefan F. Graebe and Mario E. Salgado, Pearson, 2000.
- 5. Feedback Control of Dynamic Systems by Gene F. Franklin, J.D. Powell and Abbas Emami-Naeini, 6th Edition, Pearson, 2010.

Course Outcomes:

After the completion of the course students will able to:

- **CO1:** Understand the concepts of control systems feedback effect, mathematical modelling, and time response.
- **CO2:** Apply the concepts of Block diagram reduction, Signal flow graph method for obtaining mathematical and Root locus, Bode, Nyquist, Polar plots for stability calculations.
- **CO3:** Apply the concept of controllability and observability and demonstrate the use of these techniques.
- **CO4:** Analyze time response analysis, error constants, and stability characteristics of a given mathematical model using different methods.
- **CO5:** Design and develop different compensators, controllers and their performance evaluation for various conditions.
- **CO6:** Implement different compensators and controllers in solving various engineering applications.

Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0421T	3:0:0	3	CIE:30	3 Hours	PCC
			SEE:70		

ANTENNAS & MICROWAVE ENGINEERING

Course Objectives:

- To allow the student to understand the basic principles in antenna and micro wave system design.
- To make the student to gain knowledge in various antenna designs.
- To enable the student knowledge in the area of microwave components and antenna for practical applications.

Syllabus	Total Hours: 48
Unit –I	10 Hrs
	• 1 • 7 •

Introduction to Antennas: Definition of antenna, Radiation Mechanism – single wire, two wire, dipoles, Antenna Parameters - Radiation Patterns, Main Lobe and Side Lobes, Beam widths, Beam Area, Radiation Intensity, Beam Efficiency, Directivity, Gain and Resolution, Aperture Efficiency, Effective Height and length, Radiation from Small Electric Dipole, Quarter wave Monopole and Half wave Dipole– Current Distributions, Field Components, Radiated power, Radiation Resistance, Loop Antennas - Introduction, Small Loop, Comparison of far fields of small loop and short dipole, Radiation Resistances and Directives of small and large loops (Qualitative Treatment)

Unit –II	10 Hrs		
VHF, UHF and Micro wave Antennas: Helical Antennas-Helical Geome	ry, Helix modes, Horn		
Antennas- Types, Fermat's Principle, Optimum Horns, Design considerations of Pyramidal Horn			
Micro strip Antennas- Introduction, features, advantages and limitatio	ns, Rectangular patch		
antennas-Geometry and parameters, characteristics of Micro strip antenna	s, parabola reflectors-		
geometry, pattern characteristics, Feed Methods, Reflector Types - Related F	eatures.		

Unit –III10 HrsAntenna Arrays and propagation: Arrays of 2 Isotropic sources- Different cases, Principle of
Pattern Multiplication, Uniform Linear Arrays – Broadside Arrays, End fire Arrays, EFA with
Increased Directivity, Derivation of their characteristics and comparison, Arrays with Parasitic
Elements-Yagi- Uda Arrays, Folded Dipoles & their characteristics Ground wave propagation
Space wave propagation-Sky wave propagation(Qualitative treatment).

Waveguides: Introduction, Rectangular wave guides, Field expressions for TE and TM modes, Wave propagation in the guide, Phase and group velocities, Wave guide current and mode excitation.

Unit –IV	9 Hrs			
Microwave Components: Introduction to scattering parameters and their properties, Hybrid Tees				
(H-plane, E-plane, Magic Tees), Hybrid ring, Directional Couplers - Bethe hole and Two-ho				
Couplers, Deriving Scattering matrix for H-plane, E-plane, Magic Tees.				

Micro wave Amplifiers and Oscillators: Micro wave Tubes: Linear Beam Tubes–Two cavity Klystron amplifier -velocity modulation, bunching process, Reflex Klystron oscillator, Travelling Wave Tube (TWT) – Bunching process and amplification process (Qualitative treatment only).

Crossed Field Tubes-Magnetron oscillator, pi-mode operation, Hartree Condition.

Unit –V	9 Hrs
Micro wave Semi conductor Devices: Gunn Oscillator – Principle of ope	ration, Characteristics,
Two valley model. Antennas and Microwave Measurements: Sources of	errors, Patterns to be
Measured, Pattern Measurement Arrangement, Directivity Measurement, G	ain Measurements (by
comparison, Absolute and 3-Antenna Methods). Description of Microwave	bench-different blocks
and their features, errors and precautions, Measurement of attenuation, fro	equency, VSWR (low,
medium, high), Impedance measurements.	
Textbooks:	

- 1. JohnD.Kraus, Ronald J. Marhefkaand Ahmad S. Khan, "Antennas and Wave propagation", TMH New Delhi, 4thEd, 2010.
- 2. SamuelY. Liao, "Micro wave devices and circuits", 3rdEdition, Pearson Publishing, 2003.

References:

1. R .E. Collin, "Foundations for micro wave engineering", 2ndEdition, JohnWiley,2002.

- 2. C.A. Balanis, "Antenna Theory-Analysis and Design", John Wiley & Sons, 2nd Edn. 2001.
- 3. M. Kulkarni, "Micro wave and Radar Engineering", Umesh Publications, 4th edition 2009.

4. G.S.NRaju, "AntennaandWavePropagation",PearsonEducationIndia,3rdEdition2009.

Course Outcomes:

After the completion of the course students will able to:

CO1: Understand the generation of radiation and basic concepts of dipole and loop Antennas

CO2: Analyze the Practical antenna design characteristics to meet the requirements of modern wireless communications.

CO3: Understand the uses of antenna arrays and waveguides for propagation of EM wave.

CO4: Analyze various microwave components and the principles of different microwave sources.

CO5: Gain knowledge on Micro wave Amplifiers and Oscillators.

CO6: Measure the different Parameters of antennas and propagation of microwaves through waveguides.

DATA COMMUNICATION AND NETWORKS

Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0422T	3:0:0	3	CIE:30	3 Hours	PEC
			SEE:70		

Course Objectives:

The student should be made to:

- Understand the division of network functionalities into layers.
- Be familiar with the components required to build different types of networks
- Be exposed to the required functionality at each layer
- Learn the flow control and congestion control algorithms

Syllabus	Total Hours: 48
Unit –I	10 Hrs

Data Communications: Components, protocols and standards, Network and Protocol Architecture, Reference Model ISO-OSI, TCP/IP-Overview, topology, transmission mode, digital to digital encoding, transmission media guided and unguided, Switching: Circuit switching(space-division, time division and space-time division), packet switching (virtual circuit and Data gram approach), message switching.

Unit –II	10 Hrs
Data Link Layer: Design issues, Data Link Control and Protocols: Flow and	Error Control, Stop-
and wait ARQ. Sliding window protocol, Go-Back-N ARQ, Selective Repeat A	RQ, HDLC, Point-to
-Point Access: PPP Point -to- Point Protocol, PPP Stack.	

Unit –III	10 Hrs
Medium Access Sub layer: Channel allocation problem, Controlled Acc	cess, Channelization,
multiple access protocols, IEEE standard 802.3 & 802.11 for LANS and WLA	N, high-speed LANs,
Bluetooth IEEE 802.16.Tokenring, Token Bus, FDDI based LAN, Networ	rk Devices-repeaters,
hubs, switches bridges.	

Unit –IV9 HrsNetwork Layer: Design issues, Routing algorithms, Congestion control algorithms, Host to Host
Delivery: Internetworking, addressing and routing, IP addressing (class full & Classless), Subnet,
Network Layer Protocols: ARP, IPV4, ICMP, IPV6 and ICMPV6.

Unit –V9 HrsTransport Layer: Process to Process Delivery: UDP; TCP, congestion control and Quality of
service. Application Layer: Client Server Model, Socket Interface, Domain Name System (DNS):
Electronic Mail (SMTP), file transfer (FTP), HTTP and WWW.

Text Books:

1. S. Tannenbum, D. Wetherall, —Computer Networksl, Prentice Hall, Pearson, 5thEd.

2. Behrouz A. Forouzan, —Data Communications and Networkingl, Tata McGraw-Hill, 4th Ed

References:

1. Fred Halsall, -Computer Networksl, Addison - Wesley Pub. Co. 1996.

- 2. Larry L, Peterson and Bruce S. Davie, —Computer Networks: A system Approachl, Elsevier, 4thEd
- 3. Tomasi, —Introduction To Data Communications & Networking|, Pearson 7th impression2011.
- 4. William Stallings, —Data and Computer Communications, Prentice Hall, Imprint of Pearson,

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Course Outcomes:

After the completion of the course students will able to:

CO1: Understand the basics of data communication, networking, internet and their importance.

CO2: Analyze the services and features of various protocol layers in data networks.

CO3: Differentiate wired and wireless computer networks

CO4: Analyze TCP/IP and their protocols.

CO5: Understand the flow control and congestion control algorithms

CO6: Understand different internet devices and their functions.

CONTRACT OF STREET

GEETHANJALI INSTITUTE OF SCIENCE & TECHNOLOGY: NELLORE DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING B. Tech ECE – BG 22 Regulation

			CE – RG 22 Regula		
	IN	FORMATIC	ON THEORY ANI) CODING	
Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0423T	3:0:0	3	CIE:30 SEE:70	3 Hours	PEC
Course Objectiv	ves:				I
		ameters of In	nformation, the con	cepts of source codi	ing techniques and
	trol coding te			1	0 1
• To transr	nit knowledg	ge on Informa	ation theory and er	ror control coding t	echniques to solve
problems					
			-	ing techniques for e	error detection and
			bearing signals.		
To descri	be various sy			convolutional code	
		Syllab			Total Hours: 48
		Unit -			10 Hrs
				conditional Entropy,	
1	1		,	n Inequalities, Probl	U
				Random Variable,	
				Single Random Var	
		ano Coding	- Free fix code, Co	oding an information	Source, Huffman
Coding, Example		Coding: Pro	\mathbf{n} ar massage set Λ	ssigning probabilitie	es to K ary rooted
				ing of a proper me	
message set, Tur		er message s		ing of a proper me	ssage set, Tunstan
message set, run	istun counig.	Unit –	-11		10 Hrs
Asymptotic Ear	ii-partition l			y, Weak law of large	
	-			of Asymptotic Equi	
Problem solving.		U	1	, I I	1 1 2
Universal Sour	ce Coding: 1	Lempel –Ziv	Algorithm, LZ – 7	77 Encoding and De	ecoding, Lempel –
Ziv Welch (LZW	/) Algorithm,	, LZW Encod	ling, and Decoding.		
-		•		Channel Coding The	
Entropy, Gaussia	ın Channel, F	Rate Distortio	n Theory, Blahut –	Arimoto Algorithm	Ŭ Ŭ
		Unit –			10 Hrs
	0			s, Error Probability	-
		· · · ·	Ũ	or Error Detection,	Ũ
			-	he upper bound of	the Probability of
Error with Codin	ig, Soft Decis		g, Hard Decision De	ecoding.	0.11
Liner Disels		Unit –		1	9 Hrs
				des, Syndrome and	
-		-	-	rity Check bit Code and Parity-Check I	-
	· ·	•		Reed-Solomon Cod	•
Codes, Elicounig	, and Decoun			Keeu-Solomon Cou	1
<u>a</u>	<u>a 11</u>	Unit -		1	9 Hrs
	0			nvolutional Code	
-	-		-	s, The Viterbi Algor	-
		Uncoded T	ransmission, Turbo	Codes, LDPC cod	es, Hard and Soft
Decision Decodi	ng.				
Text Books:	wer Iov A	Thomas El	aments of Informat	ion Theory, John V	Viley & Song 2nd
Edition, 2006	-	montas, El	monto or mormat	ion rhoury, joint v	, ney & 50118, 2
Lanion, 2000	•				

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2. Herbert Taub, Donald L Shilling, Goutam Saha, Principles of Communication Systems, 4th Edition, McGraw Hill, 2017.

References:

- 1. Shu Lin, Daniel J. Costello Jr., Error Control Coding, Pearson, Second Edition, 2013.
- 2. Simon Haykin, Communication Systems, John Wiley, 4th Edition, 2010.

Course Outcomes:

After the completion of the course students will able to:

- **CO1:** Describe the basic parameters of Information, the concepts of source coding techniques, and Error Control coding techniques.
- **CO2:** Apply the knowledge of Information theory and error control coding techniques to solve problems.
- **CO3:** Analyze various source coding and channel coding techniques for error detection and error correction in the information bearing signals.
- **CO4:** Compare various block to variable length coding and variable to block length coding techniques for merits and demerits. Also compare the performance of linear block codes and convolutional codes.
- **CO5:** Design various systems for linear block codes and convolutional codes.
- **CO6:** Implement the various source coding methods to improve the efficiency of information theory.



GEETHANJALI INSTITUTE OF SCIENCE & TECHNOLOGY: NELLORE DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

B. Tech ECE – RG 22 Regulation INDUSTRIAL ELECTRONICS

Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0424T	3:0:0	3	CIE:30	3 Hours	PEC
			SEE • 70		

Course Objectives:

- Describe semi-conductor devices (such as PN junction diode & Transistor) and their switching characteristics.
- Understand the characteristics of AC to DC converters.
- Understand about the practical applications Electronics in industries.
- Describe the ultrasonic and its application.

• Describe the ultrasonic and its application.	
Syllabus	Total Hours: 48
Unit –I	10 Hrs
Scope of industrial Electronics, Semiconductors, Merits of semiconductors, cry	stalline structure
Intrinsic semiconductors, Extrinsic semiconductors, current flow in semi conductor	or, Open circuited
p-n junction, Diode resistance, Zener diode, Photo conductors and junction photo	oto diodes, Photo
voltaic effect, Light emitting diodes(LED).	
Unit –II	10 Hrs
Introduction, The junction transistor, Conventions for polarities of voltages an	d currents, Ope
circuited transistor, Transistor biased in the active region, Current componer	its in transistors
Currents in a transistor, Emitter efficiency, Transport factor and transistor- α ,	Dynamic emitte
resistance, Transistor as an amplifier, Transistor construction, Letter symbols for	
Devices, Characteristic curves of junction transistor in common configuration, st	atic characteristi
curves of PNP junction transistor in common emitter configuration, The trans	sistor in commo
collector Configuration.	
Unit –III	10 Hrs
AC to DC converters- Introduction, Classification of Rectifiers, Half wave Rec	tifiers, Full wav
Rectifiers, Comparison of Half wave and full wave rectifiers, Bridge Rectifiers,	, Bridge Rectifie
meter, Voltage multiplying Rectifier circuits, Capacitor filter, LC Filter, Metal Rec	tifiers, Regulate
Power Supplies, Classification of Voltage Regulators, Short period Accuracy of	
period .Accuracy of Voltage Regulator, Principle of automatic voltage Regula	
Voltage stabilizer using Zener diode, D.C. Voltage Regulators, Series Voltage Reg	ulators, Complet
series voltage regulator circuit, Simple series voltage regulator.	1
Unit –IV	9 Hrs
Resistance welding controls: Introduction, Resistance welding process, Basic	
resistance welding, Types of Resistance welding, Electronic welding control us	
welding, Energy storage welding. Induction heating: Principle of induction he	
Induction heating merits of induction heating, Application of induction heating	
power source of induction heating. Dielectric heating: Principle of dielectric h	
dielectric heating, dielectric properties of typical materials, electrodes used in c	0
method of coupling of electrodes to the R.F. generator, Thermal losses in D	helectric heating
Applications.	0.77
Unit –V	9 Hrs
Ultrasonics: Introduction, Generation of Ultrasonic waves, Application of U	
Ultrasonic stroboscope, ultrasonic as means of communication, ultrasonic flaw of	detection, Optica

Ultrasonic stroboscope, ultrasonic as means of communication, ultrasonic flaw detection, Optical image on non-homogeneities, ultrasonic study of structure of matter, Dispersive study of structure of matter, Dispersive and colloidal effect of Ultrasonic, Coagulating action of Ultrasonic, separation of mixtures by ultrasonic waves, cutting and machining of hard materials by ultrasonic vibrations, Degassing of liquids by ultrasonic waves, Physio-chemical effects of ultrasonics, chemical effects of

ultrasonics, Thermal effects of ultrasonics, soldering and welding by ultrasonics, Ultrasonic Drying

Textbooks:

- 1. Fundamentals of Industrial Electronics, Bogdan M Wilamowski, J David irwin, 2nd Edition, 2011.
- 2. Industrial and Power Electronics G. K. Mithal and Maneesha Gupta, Khanna Publishers, 19th Ed., 2003.
- 3. Integrated Electronics J. Millman and C.C Halkias, McGraw Hill, 1972.

References:

- 1. Electronic Devices and circuits Theodore. H. Bogart, Pearson Education, 6th Edn., 2003.
- 2. Integrated Circuits and Semiconductor Devices Deboo and Burroughs, ISE

Course Outcomes:

After the completion of the course students will able to:

- **CO1:** Understand the semi-conductor devices and their switching characteristics.
- **CO2:** Apply the Ultrasonic waves with different applications.
- CO3: Understand the working of Transistor and its different configurations.
- CO4: Analyze the thermal effects of ultrasonic, soldering and welding by ultrasonic, ultrasonic

Drying in the industry; interpret the characteristics of AC to DC converters.

CO5: Develop the practical applications Electronics in industries.

CO6: Apply the process of Resistance welding, Induction heating and Dielectric heating in the industry.

GEETHANJALI INSTITUTE OF SCIENCE & TECHNOLOGY: NELLORE DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

		B. Tech EC	TECTURE & OI		
Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0425T	3:0:0	3	CIE:30	3 Hours	PEC
	2.0.0		SEE:70	C HOUIS	120
Course Objectiv	es:				
•		is to introd	uce principles of	computer organizat	ion and the basic
architectural conc			1 1	1 0	
	-	Syllabu	IS		Total Hours: 48
		Unit –	I		10 Hrs
Digital Computer	rs: Introductio	n, Block d	iagram of Digital	Computer, Definit	tion of Computer
				Register Transfer La	
operations: Regis	ter Transfer la	inguage, Re	gister Transfer, B	bus and memory tra	nsfers, Arithmetic
Micro operations,	logic micro o	perations, s	hift micro operation	ons, Arithmetic logi	c shift unit. Basic
Computer Organi	zation and De	sign: Instruc	ction codes, Comp	outer Registers Com	puter instructions,
Timing and Cor	ntrol, Instruction	on cycle, M	Memory Referenc	e Instructions, Inp	ut – Output and
Interrupt.					
		Unit –I	Ι		10 Hrs
Micro programme	ed Control: Co	ntrol memor	ry, Address sequer	ncing, micro program	n example, design
of control unit.	Central Proce	essing Unit:	General Register	r Organization, Ins	truction Formats,
Addressing mode		126 .			
Addressing mode	s, Data Transfé	er and Manip	pulation, Program	Control.	
Addressing mode	s, Data Transfe	er and Manıp Unit –I		Control.	10 Hrs
		Unit –I	II	Control. Point Representation	
Data Representa	tion: Data ty	Unit –I pes, Compl	II lements, Fixed P		n, Floating Point
Data Representa Representation.	tion: Data ty Computer Ar	Unit –I pes, Compl ithmetic: A	II lements, Fixed P addition and sub	Point Representation	n, Floating Point ation Algorithms,
Data Representa Representation.	tion: Data ty Computer Ar 1ms, Floating	Unit –I pes, Compl ithmetic: A – point Ari	II lements, Fixed P addition and sub athmetic operation	Point Representation ptraction, multiplica	n, Floating Point ation Algorithms,
Data Representa Representation. Division Algorith Arithmetic operat	tion: Data ty Computer Ar nms, Floating ions.	Unit –I pes, Compl ithmetic: A – point Ari Unit –I	II lements, Fixed P addition and sub ithmetic operation	Point Representation otraction, multiplica s. Decimal Arithme	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org	tion: Data ty Computer Ar nms, Floating ions. anization: Inpu	Unit –I pes, Compl ithmetic: A – point Ari Unit –I it-Output In	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, N	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer,
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org	tion: Data ty Computer Ar nms, Floating ions. anization: Inpu	Unit –I pes, Compl ithmetic: A – point Ari Unit –I it-Output In	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron	Point Representation otraction, multiplica s. Decimal Arithme	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer,
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup	tion: Data ty Computer Ar ams, Floating ions. anization: Inpu t Direct mem	Unit –II pes, Compl ithmetic: A – point Ari Unit –I it-Output In hory Access	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer,
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup	tion: Data ty Computer Ar ams, Floating ions. anization: Inpu t Direct mem	Unit –II pes, Compl ithmetic: A – point Ari Unit –I it-Output In hory Access	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer,
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian	tion: Data ty Computer Ar ms, Floating ions. anization: Inpu t Direct mem ry memory, As	Unit –I pes, Compl ithmetic: A – point Ari Unit –I nory Access sociate Men Unit –V	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memor	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian Reduced Instructi	tion: Data ty Computer Ar ms, Floating ions. anization: Input t Direct memory, As on Set Compu-	Unit –II pes, Compl ithmetic: A – point Ari Unit –I nt-Output In nory Access sociate Men Unit –V ter: CISC C	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo V haracteristics, RIS	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory ory.	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs ipeline and Vector
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian Reduced Instructi Processing: Parall Vector Processin	tion: Data ty Computer Ar ms, Floating ions. anization: Input t Direct mem ry memory, As on Set Compu- lel Processing, ng, Array Pr	Unit –II pes, Compl ithmetic: A – point Ari Unit –I nt-Output In nory Access sociate Men Unit –V ter: CISC CI Pipelining, J ocessor. M	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo V haracteristics, RIS Arithmetic Pipelin Iulti Processors:	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory ory. C Characteristics. Pi e, Instruction Pipelin Characteristics of	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs ipeline and Vector ne, RISC Pipeline, Multiprocessors,
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian Reduced Instructi Processing: Parall Vector Processin Interconnection	tion: Data ty Computer Ar ms, Floating ions. anization: Input Direct mem ry memory, As on Set Compu- lel Processing, ng, Array Pr Structures, I	Unit –II pes, Compli ithmetic: A – point Ari Unit –I it-Output In nory Access sociate Men Unit –V ter: CISC Cl Pipelining, A ocessor. M	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo V haracteristics, RIS Arithmetic Pipelin Iulti Processors:	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory ory. C Characteristics. Pi e, Instruction Pipelin	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs ipeline and Vector ne, RISC Pipeline, Multiprocessors,
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Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian Reduced Instructi Processing: Parall Vector Processin Interconnection	tion: Data ty Computer Ar ms, Floating ions. anization: Input Direct mem ry memory, As on Set Compu- lel Processing, ng, Array Pr Structures, I	Unit –II pes, Compli ithmetic: A – point Ari Unit –I it-Output In nory Access sociate Men Unit –V ter: CISC Cl Pipelining, A ocessor. M	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo V haracteristics, RIS Arithmetic Pipelin Iulti Processors:	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory ory. C Characteristics. Pi e, Instruction Pipelin Characteristics of	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs ipeline and Vector ne, RISC Pipeline, Multiprocessors,
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian Reduced Instructi Processing: Parall Vector Processin Interconnection synchronization, O Textbooks:	tion: Data ty Computer Ar ams, Floating ions. anization: Input Direct mem ry memory, As on Set Compu- lel Processing, ng, Array Pr Structures, I Cache Coheren	Unit –II pes, Compl ithmetic: A – point Ari Unit –I at-Output In hory Access sociate Men Unit –V ter: CISC CI Pipelining, A ocessor. M Interprocessor	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo V haracteristics, RIS Arithmetic Pipelin Iulti Processors: or arbitration,	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory ory. C Characteristics. Pi e, Instruction Pipelin Characteristics of	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs ipeline and Vector ne, RISC Pipeline, Multiprocessors, nmunication and
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian Reduced Instructi Processing: Parall Vector Processin Interconnection synchronization, O Textbooks: 1. Computer Sy	tion: Data ty Computer Ar ams, Floating ions. anization: Input Direct mem ry memory, As on Set Compu- lel Processing, ng, Array Pr Structures, I Cache Coheren	Unit –II pes, Compl ithmetic: A – point Ari Unit –I at-Output In hory Access sociate Men Unit –V ter: CISC CI Pipelining, A ocessor. M Interprocessor	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo V haracteristics, RIS Arithmetic Pipelin Iulti Processors: or arbitration,	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory ory. C Characteristics. Pi e, Instruction Pipelin Characteristics of Interprocessor con	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs ipeline and Vector ne, RISC Pipeline, Multiprocessors, nmunication and
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian Reduced Instructi Processing: Parall Vector Processin Interconnection synchronization, O Textbooks: 1. Computer Sy References:	tion: Data ty Computer Ar ams, Floating ions. anization: Input t Direct mem ry memory, As on Set Comput lel Processing, ng, Array Pr Structures, I Cache Coheren stem Architect	Unit –II pes, Compli ithmetic: A – point Ari Unit –I nt-Output In nory Access sociate Men Unit –V ter: CISC CI Pipelining, J ocessor. M Interprocesson interprocesson	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo V haracteristics, RIS Arithmetic Pipelin Iulti Processors: or arbitration, T	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory ory. C Characteristics. Pi e, Instruction Pipelin Characteristics of Interprocessor con	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs ipeline and Vector ne, RISC Pipeline, Multiprocessors, nmunication and
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian Reduced Instructi Processing: Parall Vector Processin Interconnection synchronization, O Textbooks: 1. Computer Sy References:	tion: Data ty Computer Ar ams, Floating ions. anization: Input t Direct mem ry memory, As on Set Comput lel Processing, ng, Array Pr Structures, I Cache Coheren stem Architect	Unit –II pes, Compli ithmetic: A – point Ari Unit –I nt-Output In nory Access sociate Men Unit –V ter: CISC CI Pipelining, J ocessor. M Interprocesson interprocesson	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo V haracteristics, RIS Arithmetic Pipelin Iulti Processors: or arbitration, T	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory ory. C Characteristics. Pi e, Instruction Pipelin Characteristics of Interprocessor con Edition, Pearson/PHI	n, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs ipeline and Vector ne, RISC Pipeline, Multiprocessors, nmunication and
Data Representa Representation. Division Algorith Arithmetic operat Input-Output Org Priority Interrup Memory, Auxilian Reduced Instructi Processing: Parall Vector Processin Interconnection synchronization, O Textbooks: 1. Computer Sy References: 1. Computer Org Hill.	tion: Data ty Computer Ar ams, Floating ions. anization: Input t Direct mem ry memory, As on Set Comput lel Processing, ng, Array Pr Structures, I Cache Coheren stem Architect ganization – Ca	Unit –II pes, Compli ithmetic: A – point Ari Unit –I nt-Output In nory Access sociate Men Unit –V ter: CISC Cl Pipelining, J ocessor. M Interprocesson ice.	II lements, Fixed P addition and sub ithmetic operation V terface, Asynchron s. Memory Orga nory, Cache Memo V haracteristics, RIS Arithmetic Pipelin Iulti Processors: or arbitration, T oris Mano, Third E	Point Representation otraction, multiplica s. Decimal Arithme nous data transfer, M nization: Memory ory. C Characteristics. Pi e, Instruction Pipelin Characteristics of Interprocessor con Edition, Pearson/PHI	h, Floating Point ation Algorithms, etic unit, Decimal 9 Hrs Modes of Transfer, Hierarchy, Main 9 Hrs ipeline and Vector he, RISC Pipeline, Multiprocessors, nmunication and
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After the completion of the course students will able to:

- CO1: Understand the basics of instructions sets and their impact on processor design.
- **CO2:** Demonstrate an understanding of the design of the functional units of a digital computer system.
- **CO3:** Evaluate cost performance and design trade-offs in designing and constructing a computer processor including memory.

CO4: Design a pipeline for consistent execution of instructions with minimum hazards.

CO5: Recognize and manipulate representations of numbers stored in digital computers.



GEETHANJALI INSTITUTE OF SCIENCE & TECHNOLOGY: NELLORE DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

	D		CE – RG 22 Regula MANAGEMENT		
		(Common	to CE,EEE,ME ar	nd ECE)	
Course Code	L:T:P:S	Credits	Exam Marks	Exam Duratio	on Course Type
22A0512T	3:0:0:0	3	CIE: 30 SEE:70	3 Hours	OEC
Course Objecti	ves:				L
To design dateTo construct	role of datal atabases usin t database qu	base manager g data model eries using re	ment system in an c ling and Logical dat elational algebra an	tabase design tech d calculus and SO	-
		ecurity mech	latabase transaction	1.	
	ze database s	Syllabu			Total Hours:48
Unit -I	Introd	V	s itabase concepts ai	nd Modeling	10tal 110tal 3.40
•	-			•	ER Design, Entities, Design with the ER
Unit -II	ŀ	Relational M	odel, Relational A	lgebra	9Hrs
Relational Algor		ction to Rela	tional algebra, sele	ection and projec	tion, set operations,
	livision.				
	livision.		SQL		10Hrs
renaming, joins, c Unit -III SQL: Basic form queries, Operator	of SQL Ques, predefined	functions, A	ML queries, Views ggregate Functions		
renaming, joins, c Unit -III SQL: Basic form queries, Operator PL/SQL: Introdu	of SQL Ques, predefined	functions, A	ML queries, Views ggregate Functions dures, Triggers, Cu		Nested & Correlated
renaming, joins, c Unit -III SQL: Basic form queries, Operator PL/SQL: Introdu Unit -IV	of SQL Ques, predefined	functions, A ons & Procee N	ML queries, Views ggregate Functions dures, Triggers, Cur formalization	rsors.	Nested & Correlated 9Hrs
renaming, joins, c Unit -III SQL: Basic form queries, Operator PL/SQL: Introdu Unit -IV Relational datal	of SQL Ques, predefined ction, Function base design:	functions, A ons & Procee N Introductio	ML queries, Views ggregate Functions dures, Triggers, Cur formalization n, Functional Dep	rsors.	Nested & Correlated
renaming, joins, c Unit -III SQL: Basic form queries, Operator PL/SQL: Introdu Unit -IV Relational databas	of SQL Ques, predefined ction, Functi base design: es: 1NF, 2N	functions, A ons & Proces N Introductio F, 3NF and I Introduction Mana	ML queries, Views ggregate Functions dures, Triggers, Cur formalization n, Functional Dep	rsors. endencies (FDs) tions of Multi Va	Nested & Correlated 9Hrs , Normalization for

Text Books:

- 1. Abraham Silberschatz, Henry F. Korth, S. Sudarshan, Database System Concepts, 6th Edition, Tata McGraw-Hill Publishing Company,2017.
- 2. Raghu Ramakrishnan, Database Management System, 3rd Edition, Tata McGraw-Hill Publishing Company, 2014.

Reference Books:

- 1. Peter Rob, A.Ananda Rao, Corlos Coronel, Database Management Systems (for JNTU), Cengage Learning, 2011.
- 2. Hector Garcia Molina, Jeffrey D. Ullman, Jennifer Widom, Database System Implementation, 1st Edition, Pearson Education, United States, 2000.
- 3. E. Ramez and Navathe, Fundamental of Database Systems, 7th Edition, Pearson Education
- R.P. Mahapatra & Govind Verma, Database Management Systems, Khanna Publishing House, 2016.
- 5. Carlos Coronel and Steven Morris, Database Systems: Design, Implementation, and Management, 12th edition, Cengage Learning, 2016.
- 6. John V. , Absolute beginner's guide to databases, Petersen, QUE

Web Resources:

- 1. https://www.coursera.org/learn/database-management
- 2. https://www.coursera.org/learn/sql-data-science
- 3. https://www.w3schools.com/sql/
- 4. https://www.youtube.com/watch?v=fHAfc7Hjq28&list=PLWPirh4EWFpGrpcMfZ6UcdI786 QdtSxV8
- 5. https://www.youtube.com/watch?v=HwmEcudlv44&list=PL4OCRJojkV1jN-Ed6RkQpWfBvqe0utRd6
- 6. http://www.w3schools.in/dbms/
- 7. https://www.geeksforgeeks.org/dbms/
- 8. https://www.javatpoint.com/dbms-tutorial
- 9. https://www.edureka.co/blog/dbms-tutorial/

Course Outcomes (CO):

On completion of this course, student will be able to:

CO1: Understand the Basic Concepts of Database languages, Relational model, SQL.

CO2: Choose the specific Data models for large enterprise database design.

CO3: Analyze the data efficiently through SQL instructions.

CO4: Apply Normal forms on database for eliminating the redundancy.

CO5: Demonstrate the Basic Concepts of transaction management techniques.

CO6: Apply concurrency control techniques for Database recovery.

CONTRACT OF THE OWNER

GEETHANJALI INSTITUTE OF SCIENCE & TECHNOLOGY: NELLORE DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING B. Tach ECE. BC 22 Population

Course CodeL:T:P22A0258T3:0:0Course Objectives:Student will be able to,1. To develop the understand conditions.2. To understand the conce	(Comme	R ELECTRONICS on to all Except EI Exam marks CIE:30	EE) Exam Durat		Course Type
22A0258T3:0:0Course Objectives:Student will be able to,1. To develop the understand conditions.	Credits	Exam marks CIE:30	Exam Durat	tion	Course Type
Course Objectives: Student will be able to, 1. To develop the understan conditions.	3		2 11		
Student will be able to, 1. To develop the understan conditions.		SEE:70	3 Hours		OEC
1. To develop the understan conditions.		1			
conditions.					
2. To understand the conce	nding of uncomper	sated lines and thei	r behavior unde	r heav	y loading
3. To emphasize the object			e operation of S		
	Syllabus			Tota	l Hours: 49 Hrs
Unit-I		considerations and			10 Hrs
Transmission Interconnecti	,	•		•	5
Considerations of a Transn		· I I		t comp	ensation, Basic
Types of FACTS Controlle	rs, Benefits from F	FACTS, Application	n of FACTS.		
Unit-II		t Compensators			8 Hrs
Objectives of Shunt Compe					
Voltage Support to Prevent					
Damping, Static Var Comp			-	-	
and dynamic Performance, Unit -III			Power Oscillati	ion Da	<u>mping.</u> 10 Hrs
	Serie	s Compensators			IU HIS
Objectives of Series Com	pensation, concep	ot of series capacit	tive compensati	ion, vo	oltage stability,
improvement of transient		-	-		•
capacitor, Thyristor control			•		
Unit -IV	Combir	ned Compensators			10 Hrs
Introduction, Unified powe	r flow controller,	basic operating pri	nciples, indeper	ndent r	eal and reactive
power flow control, and con		1 01	1 1		
Unit -V		rollers & Cyclo Co	-		10 Hrs
Power quality problems, ha	8	v		ow. ar	
harmonics, filters, passive f		0	1	o , , , , , , , , , , , , , , , , , , ,	
		, shane, series and i			
Textbooks:					
1. Narain G. Hingorani, La				4 1	Electricel Derror
2. Roger. C. Dugan, Mark		am, Surya Santoso	, н. wayne в	eaty, I	Electrical Power
Systems Quality, McGra	w Hill,2003				
References:					
1. Y. H. Song, A. T. Johns 2010.	, Flexible A. C. T	ransmission Systen	n, IEE, London,	19991	Edition, Pearson,
Course Outcomes(CO):					
At the end of studying the c	course, the student	should be able to:			
CO1: Choose proper control			on system reau	iremer	nts
CO2: Understand various s	1	1 1	• 1		
		Controllers SVC &		vorio	с .: ·

8

Band Renting and the

		B. Tech ECI	E – RG 22 Regulati	ion		
		DAMENTAL	S OF DRONE TE	CHNOLOGY		
Course Code	L:T:P	Credits	Exam marks	Exam Dura	tion	Course Type
22A0334Tc	2: 1:0	3	CIE:30 3 Hours	OEC		
			SEE:70			
Course Objectiv						
The course should						
			asic concepts of UA	V drone system	ns.	
To introduce	e the stability	and control of				
		Syllabu	S		Tota	ll Hours: 50
UNIT-I		Introdu	ction to Drones		10 H	ſrs
Introduction to U	Jnmanned Air	craft Systems,	History of UAV dr	ones, classificat	tion of	f drones, System
Composition, ap	plications					
UNIT-II		Design of U	AV Drone System	s	10H	rs
Introduction to	Design and S	election of the	e System, Aerodyna	amics and Airf	rame	Configurations,
	of Aircraft Typ	pes, Design St	andards and Regula	atory Aspects-In	ndia S	pecific, Design
for Stealth.					1	
UNIT-III		Avionics H	ardware of Drones	5	10H	rs
Autopilot, AGL-	pressure sense	ors servos-acce	elerometer –gyros-a	ctuators- power	supp	ly-processor,
integration, insta	-			-		
UNIT-IV	Co	mmunication	, Payloads and Co	ntrols	10H	rs
Communication,	, Payloads and	Controls: Pay	loads, Telemetry, T	racking, contro	ls-PID) feedback,
	quency range,	modems, men	nory system, simula	tion, ground tes	st-anal	ysis-trouble
shooting						
UNIT-V			ion and Testing		10H	
-	• • • •	-	on, ground control s	oftware, System	n Grou	and Testing,
System In-flight	Testing, Futu	re Prospects ai	nd Challenges			
Textbooks:						
1. Reg Austin "	Unmanned Ai	rcraft Systems	UAV design, devel	opment and dep	ploym	ent", Wiley,
2010.						
2. Robert C. Ne	lson. Flight St	ability and Au	tomatic Control, M	cGraw-Hill. Inc	. 1998	3.
	-	•	nned Aerial Vehicle			
	-				int all	
	Springer, 2007	1				
Reference Books	5:					
1. Paul G Fahlst	rom, Thomas	J Gleason, "In	troduction to UAV	Systems", UAV	/ Syst	ems, Inc, 1998
2. Dr. Armand	J. Chaput, "D	esign of Unm	anned Air Vehicle	Systems", Loci	kheed	Martin
Aeronautics.						

GEETHANJALI INSTITUTE OF SCIENCE & TECHNOLOGY: NELLORE DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING B. Tech ECE – RG 22 Regulation

Course Outcomes(CO):

At the end of studying the course, the student should be able to:

CO1: Understand the Concept of UAV, its components and its known applications.

CO2: Identify the type of drone and design a drone for a given application/specification.

CO3: Ability to design UAV drone system

CO4: To understand working of different types of engines and its area of applications.

CO5: To understand static and dynamic stability dynamic instability and control concepts

CO6: To know the loads taken by aircraft and type of construction and also construction materials.



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F ELECTRONICS AND COMMUNI
B. Tech ECE – RG 22 Regulation
DITL DINC MATERIALS

		BUILD	ING MATERIALS			
		(ME, CSE, A	I&ML, CS, DS, EC	CE, EEE)		
Course Code	L:T:P	Credits	Exam marks	Exam Dui	ration	Course Type
22A0149T	3:1:0:0	3	CIE:30 SEE:70	3 Hou	rs	OEC
Course Objective	es:					
• To identify the	he traditional m	naterials that are	e used for building c	constructions.		
• To explain ba	asic concepts o	f building com	ponents such as stain	case and ma	sonry	
		1	ares and its preventi			
To understan	nd the building		bye laws and acous	tics of buildir	-	
		Syllabus			To	otal Hours: 48
Unit-I		\mathbf{M}_{2}	ATERIALS			9 Hrs
			masonry -Brick-typ	pes of brick r	nasonry	- lime Cement –
Timber – Season	ing of timber -			0		0.11
Unit-II		_	IG COMPONENT			9 Hrs
			5 – Types. Differen			
			ed, Flat and Curved			-
Trussed roots - F	King and Queer	n Post Trusses.	Doors & Windows-	Types and Sp	pecificat	tions
Unit -III		Γ	DAMPNESS			10 Hrs
-	-		pness- ill effects o	-	-	nents of an ideal
	p proofing-mat	_	proofing -methods	of damp proc	ofing.	
Unit -IV			ING PLANNING			10 Hrs
Elements of build based on utility-o	• • •	-	ments-orientation-pl	anning for er	nergy ef	ficiency-planning
Unit -V		BUILDING R	ULES AND BYE-I	LAWS		10 Hrs
			ayouts or subdivis		ig regul	
0 0	e	0 0	nth, floor and car		0 0	
Information Syst	tem					
Textbooks:						
1. Building Dra	wing by M.G.	Shah, C.M. Ka	le and S.Y. Patki, T	ata McGraw-	Hill, Ne	w
2. B.C. Punmi	a, Ashok Ku	mar Jain and	Arun Kumar Jai	n, ,Building	Constr	ruction' - Laxmi
Publications	(P) Ltd., New	Delhi				
Reference Books	:					
1. Building Ma	terials, S. K. D	uggal, New Ag	e International Publ	ications.		
2. N. Kumarasv	wamy, A. Kame	eswara Rao, bu	ilding planning and	drawing, 7th	Ed, Cha	rotar
E-resources:						
1 http://nptel.ac.i	n/courses/1051	04103/				
2. http://www.aca	demicpub.org/	jwrhe/				

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3. http://www.peo.on.ca/index.php/ci_id/21843/la_id/1

Course Outcomes(CO):

On completion of this course, student will be able to:

CO1: To understand the characteristics of different building materials.

CO2: Differentiate brick masonry, stone masonry construction and bonds used in construction of walls of buildings.

CO3: To know about the causes of dampness in buildings and its ill effects.

CO4: To understand the principles of planning in buildings.

CO5: Describe capable of understanding building rules.

CO6: Acquire the knowledge about bye-laws and building elements.



DIGITAL SYSTEM DESIGN THROUGH VERILOG LAB

Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0426P	0:0:3	1.5	CIE:30	3 Hours	РСС
			SEE:70		

Course Objectives:

- To understand and develop HDL(Verilog) source code for the given problem/experiment
- To analyze the obtained results of the given experiment/problem
- To simulate the given circuit with suitable simulator and verify the results
- To understand how to use FPGA/CPLD hardware tools in the lab
- To design and implement the experiments using FPGA/CPLD hardware tools

Syllabus

LIST OF EXPERIMENTS: (Conduct Any 10 experiments).

- 1. Realization of Logic gates
- 2. Design and Implementation of an Inverter
- 3. Design and Implementation of Full adder.
- 4. Design and Implementation of Full Subtractor
- 5. Design and Implementation of 4-bitcomparator.
- 6. Design and Implementation of 4-bit ripple carry and carry look ahead adder
- 7. Design and Implementation of 16:1 mux through 4:1 mux
- 8. Design and Implementation of 3:8 decoder realization through 2:4 decoder
- 9. Design and Implementation of 8:3 encoder
- 10. Design and Implementation of 8-bit parity generator and checker
- 11. Design and Implementation of different Flip-Flops
- 12. Design and Implementation of 8 bit up-down counter
 - 13. Design and Implementation of 4bit sequence detector through Mealy and Moore state machines.

Software Required:

i)FPGA Programming Software like Xilinx Vivado / Altera (Intel) / Cypress / Equivalent Industry Standard Software

ii)Personal computer system with necessary software to run the programs and to implement

Course Outcomes(CO):

On completion of this course, student will be able to:

CO1: Understand HDL(Verilog) source code for the given problem/experiment

CO2: Develop HDL(Verilog) source code for the given problem/experiment

CO3: Analyze the obtained results of the given experiment/problem

CO4: Simulate the given circuit with suitable simulator and verify the results

CO5: Understand how to use FPGA/CPLD hardware tools in the lab

CO6: Design and implement the experiments using FPGA/CPLD hardware tools

ANTENNAS & MICROWAVE ENGINEERING LAB

Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0429P	0:0:3	1.5	CIE:30	3 Hours	PCC
			SEE:70		

Course Objectives:

- To make students to understand various antennas
- To understand the working of different micro wave components and verify characteristics using micro wave bench setup.

Syllabus

Part-A: Antennas Lab

- To analyze the characteristics of Simple Dipole $\lambda/2$ and $\lambda/4$ Antenna
- To analyze the variation in the Radiation Strength at given distance from Antenna
- To analyze the Reciprocity Theorem for Antennas
- To study Folded Dipole $\lambda/2$ Antenna
- Study of Yagi Uda 3element Folded Dipole, 5element folded dipole.
- To analyze the characteristics of micro strip antennas
- To analyze the characteristics and radiation pattern of broad side and end fire arrays.

Part-B: Micro wave Engineering lab

- Reflex Klystron Characteristics.
- Gunn Diode Characteristics.
- Directional Coupler Characteristics.
- VSWR Measurement.
- Measurement of Wave Guide Parameters.
- Measurement of Scattering Parameters of a Magic Tee.
- Attenuation Measurement.
- Microwave Frequency Measurement

NOTE: At least 5 Experiments from each section must be done in the semester.

Course Outcomes:

After the completion of the course students will able to:

CO1: Analyze performance characteristics of Antennas

CO2: Understand the working, different microwave components and sources in a microwave bench

CO3: Verify the characteristics of various microwave components using microwave bench setup.

CO4: Verify Theorems applicable for antennas

CO5: Measure scattering parameters of microwave components.

CO6: Measure Attenuation and frequency of microwave.

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B. Tech ECE – RG 22 Regulation SOFT SKILLS

Course Code	L:T:P	Credits	Exam. Marks	Exam Duration	Course Type
22A0029P	1:0:2	2	CIE:30	3Hours	SC
			SEE:70		

Course Objectives:

- To encourage all round development of the students by focusing on soft skills.
- To make the students aware of critical thinking and problem-solving skills.
- To develop leadership skills and organizational skills through group activities.
- To function effectively with heterogeneous teams.

Syllabus	Total Hou	rs:45
Unit -I	Soft Skills & Communication Skills	9Hrs

Introduction, meaning, significance of soft skills –Vital Components of communication skills - Inter-personal skills - Verbal and Non-verbal Communication.

Activities: Narration about self- strengths and weaknesses- clarity of thought - Interpersonal Skills-Group Discussion – Debate – Mutual Understanding - Book and film Reviews by groups - Group leader presenting views (non- controversial and secular) on contemporary issues or on a given topic. Verbal Communication- Oral Presentations- Extempore- brief addresses and speeches- Negotiation skills –Role Play- Non-verbal communication – Public speaking – Mock interviews – Anchoring Skills.

Critical Thinking	9Hrs				
Active Listening - Observation - Curiosity - Introspection - Analytical Thinking - Open-					
iinking.					
ormation and statistics on a topic - sequencing - assorting -	- reasoning –				
g the problem – finding the root cause - seeking viable solut	ion – judging				
g the views of others - Case Study, Story Analysis.					
Problem Solving & Decision Making	9Hrs				
Problem Solving – Managing Conflict – Conflict resolution	– Methods of				
decision making – Effective decision making in teams – Methods & Styles.					
Activities: Placing a problem which involves conflict of interests, choice and views – formulating					
solutions by proper reasoning - Discussion on important	professional,				
decisions and initiate debate on the appropriateness of the d	ecision. Case				
n.					
Emotional Intelligence & Stress Management	9Hrs				
inking before Reacting – Empathy for Others – Self-awarenes	ss – Self-				
rs – Controlling Stress – Tips.					
Activities: Providing situations for the participants to express emotions such as happiness,					
enthusiasm, gratitude, and sympathy, and confidence, compassion in the form of written or oral					
opportunities for the participants to narrate certain crisis and s	tress –ridden				
re, anger, jealousy, resentment and frustration in the form of v	vritten and				
zing Debates.					
	ervation – Curiosity – Introspection – Analytical Think inking. ormation and statistics on a topic - sequencing – assorting – g the problem – finding the root cause - seeking viable solut g the views of others - Case Study, Story Analysis. Problem Solving & Decision Making Problem Solving – Managing Conflict – Conflict resolution ve decision making in teams – Methods & Styles. olem which involves conflict of interests, choice and views – solutions by proper reasoning – Discussion on important decisions and initiate debate on the appropriateness of the d n. Emotional Intelligence & Stress Management inking before Reacting – Empathy for Others – Self-awareness rs – Controlling Stress – Tips. ations for the participants to express emotions such as happing d sympathy, and confidence, compassion in the form of writte opportunities for the participants to narrate certain crisis and stre, anger, jealousy, resentment and frustration in the form of v				



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Unit -V	Leadership Skills	9Hrs
Team-Building – Decisio	n-Making – Accountability – Planning – Public Speaking – M	Iotivation –
Risk Taking - Team Build	ding - Time Management.	
Activities: Forming group	p with a consensus among the participants- choosing a leader	- encouraging
the group members to exp	press views on leadership- democratic attitude- sense of sacrif	fice – sense of
adjustment – vision – acc	ommodating nature- eliciting views on successes and failures	of leadership
using the past knowledge	e and experience of the participants, Public Speaking, Activ	ities on Time
Management, Motivation,	, Decision Making, Group discussion etc.	
Text Books:		
• •	nent and Soft Skills (English, Paperback, MitraBarunK.)Publi //Cdr edition (July 22, 2012)	isher: Oxford
2. Personality Developm	nent and Soft Skills: Preparing for Tomorrow, Dr Shikha Kap	oor Publisher
• •	blishing House; 0 edition (February 28, 2018)	
References:		
 Soft skills: personality 2018. 	y development for life success by Prashant Sharma, BPB pub	lications,
2. Soft Skills By Alex K	. Published by S.Chand	
3. Soft Skills: An Integra	ated Approach to Maximise Personality Gajendra Singh Chau	ihan,
Sangeetha Sharma Pu		
	s and Soft Skills (Hardcover, A. Sharma) Publisher: Yking bo	
	BIG IMPACT (English, Paperback, RenuShorey) Publisher: N	
-	English Dr. Rajiv Kumar Jain, Dr. Usha Jain Publisher: Vayu	u Education
of India.		
Online Learning Resour		
· ·	NJtg2L8?list=PLLy_2iUCG87CQhELCytvXh0E_y-bOO1_q	
	gJZ0t6A?list=PLzf4HHlsQFwJZel_j2PUy0pwjVUgj7KlJ	
3. https://youtu.be/-Y-R		
4. https://youtu.be/gkLsi5. https://youtu.be/2bf9F		
6. https://youtu.be/Fchf		
Course Outcomes:		
	ne course students will able to :	
-	elements of effective communicative skills.	
	the emotional level through emotional intelligence.	
	king skills in problem solving.	
-	of an organization for team building.	
CO5: Judge the situation	and take necessary decisions as a leader.	
CO6: Develop social an	d work-life skills as well as personal and emotional well-bein	

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B. Tech ECE – RG 22 Regulation DESIGN THINKING AND INNOVATION

	(Commor	n to CSE, AI	ML, CS, DS, CE, I	EEE, ME and ECE)	
Course Code	L:T:P	Credits	Exam Marks	Exam Duration	Course Type
22A0526	2:0:0	0	CIE:30	-	MC
Course Objecti	ves:				
The objective of	this course	is to familiar	izo students with do	aign thinking process	as a tool for

The objective of this course is to familiarize students with design thinking process as a tool for

breakthrough innovation. It aims to equip students with design thinking skills and ignite the minds to create innovative ideas, develop solutions for real-time problems.

	Syllabus	Total Hours:48
Unit -I	Introduction to Design Thinking	9Hrs

Introduction to elements and principles of Design, basics of design-dot, line, shape, form as fundamental design components. Principles of design. Introduction to design thinking, history of Design Thinking, New materials in Industry.

Unit -II Design Thinking Process	9Hrs
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Design thinking process (empathize, analyze, idea & prototype), implementing the process in driving inventions, design thinking in social innovations. Tools of design thinking - person, costumer, journey map, brain storming, product development Activity: Every student presents their idea in three minutes, Every student can present design process in the form of flow diagram or flow chart etc. Every student should explain about product development.

	Unit -III Innovation	10Hrs
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Art of innovation, Difference between innovation and creativity, role of creativity and innovation in organizations. Creativity to Innovation. Teams for innovation, Measuring the impact and value of creativity. Activity: Debate on innovation and creativity, Flow and planning from idea to innovation, Debate on value-based innovation.

Unit -IV	Product Design	10Hrs

Problem formation, introduction to product design, Product strategies, Product value, Product planning, product specifications. Innovation towards product design Case studies. Activity: Importance of modelling, how to set specifications, Explaining their own product design.

Unit -V	Design Thinking in Business Processes	10Hrs
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Design Thinking applied in Business & Strategic Innovation, Design Thinking principles that redefine business – Business challenges: Growth, Predictability, Change, Maintaining Relevance, Extreme competition, Standardization. Design thinking to meet corporate needs.

Design thinking for Startups. Defining and testing Business Models and Business Cases. Developing & testing prototypes. Activity: How to market our own product, About maintenance, Reliability and plan for startup.

Text Books:

1. Change by design, Tim Brown, Harper Bollins (2009)

2. Design Thinking for Strategic Innovation, Idris Mootee, 2013, John Wiley & Sons



Reference Books:

- 1. Design Thinking in the Classroom by David Lee, Ulysses press
- 2. Design the Future, by Shrrutin N Shetty, Norton Press
- 3. Universal principles of design- William lidwell, kritinaholden, Jill butter.
- 4. The era of open innovation chesbrough.H

Course Outcomes (CO):

On completion of this course, student will be able to:

CO1: Define the concepts related to design thinking.

CO2: Explain the fundamentals of Design Thinking and innovation

CO3: Apply the design thinking techniques for solving problems in various sectors.

CO4: Analyze to work in a multidisciplinary environment

CO5: Evaluate the value of creativity

CO6: Formulate specific problem statements of real time issues