

B.TECH Electronics & Communication Engineering Course Structure (RG22)

Semester - 2 (Theory-4, Lab-5)									
Sl. No.	Category	Course Code	Course Title	Hours per week		Credits			
1.00		couc		L	Т	Р	С		
1	BSC	22A0002T	Differential Equations and Vector Calculus	3	0	0	3		
2	BSC	22A0006T	Chemistry	3	0	0	3		
3	ESC	22A0201T	Fundamentals of Electrical Circuits	3	0	0	3		
4	ESC	22A0401T	Electronic Devices & Circuits	3	0	0	3		
5	BSC (Lab)	22A0011P	Chemistry Lab	0	0	3	1.5		
6	ESC (Lab)	22A0202P	Fundamentals of Electrical Circuits Lab	0	0	3	1.5		
7	ESC (Lab)	22A0402P	Electronic Devices & Circuits Lab	0	0	3	1.5		
8	ESC (Lab)	22A0403P	Electronics Workshop	0	0	3	1.5		
9	ESC (Lab)	22A0502P	IT Workshop	0	0	3	1.5		
			Tot	al cred	its		19.5		

Category	Credits
Basic Science Course (BSC)	7.5
Engineering Science Course (ESC)	12
Total	19.5

Differential Equations & Vector Calculus								
Course Code	L:T:P:S	Credit	Exam marks	Exam Dura	tion	Course Type		
22A0002T	3:0:0:0	3	CIE:30 SEE:70	3 Hours	5	BS		
Course Object	tives:							
To enlighten the	e learners in t	he concept of	f differential equa	tions and mul	tivari	able calculus,		
to furnishthe lea	arners with ba	sic concepts	and techniques a	t plus two lev	el to	lead them into		
advanced level	by handling v	arious real wo	orld applications.					
Syllabus	• •		••		Tota	al Hours:45		
Module - I	Line	ar Differenti Ordei	al Equations of Constant Coeff	Higher icients)	9 Hı	rs		
Definitions, hon	nogenous and	non-homoge	nous, compliment	ary function,	gener	al solution,		
particular integr	ral, Wronskea	in, method o	of variation of pa	rameters. Sim	ultan	eous linear		
equations, Appli	ications to L-O	C-R Circuit p	roblems and Mass	spring system	1.			
Module - II		Partial D	ifferential Equat	tions	9 Hı	rs		
Introduction and	d formation of	f Partial Diff	erential Equations	by elimination	on of	arbitrary		
constants and an	bitrary functi	ons, solution	s of first order e	quations using	g Lag	grange's		
method. Non lin	nearequations	of first order	– Type I, II, III, I	V.				
Module - III	Applica	tions of Part	tial Differential I	Equations	9 Hi	rs		
Classification of Applications of Derivation), Sol	Classification of PDE, method of separation of variables for second order equations. Applications of Partial Differential Equations: One dimensional Wave equation (Without Derivation), Solutions one Dimensional Wave equation by the method of separation of							
Variables and rel	lated Problem	S. Vooton Di	Formation		011	1 0		
Scalar and yest	or point fund	tions vector	operator del del	applies to as	9 III	noint		
functions-Gradie	ent, del applie	d to vector p	oint functions-Div	ergence and C	Curl, v	/ector		
Module - V		Vector Int	tegration		9 Hi	rs		
Line integral-ci	rculation-worl	k done, surfa	ace integral-flux,	Green's theor	em in	n the plane		
(without proof).	Stoke's theo	orem (withou	t proof), volume	integral, Div	ergen	ce theorem		
(without proof)	and applicatio	ons of these th	eorems.	U ,	U			
Course Outcon	nes (CO):							
 On completion of this course, student will be able to Solve the linear differential equations with constant coefficients by appropriate method. Apply a range of techniques to find solutions of standard partial differential equations. Calcify the PDE, learn the applications of PDEs Apply del to Scalar and vector point functions, illustrate the physical interpretation of Gradient, Divergence and Curl. Apply Green's, Stokes and Divergence theorem in evaluation of double and triple integrals. 								
Textbooks:						. –		
1. B.S. Grewal,	Higher Engin	neering Math	ematics, 44/e, Kh	anna publishe	rs, 20	17.		
2. Differential E	2. Differential Equations & Vector Calculus by T.K.V. Iyengar, B.Krishna Gandhi,							
S.Ranganathamand M.V.S.S.N.Prasad S. Chand publication.								
Reference Boo	Reference Books:							
1. Erwin Kreysz	ig, Advanced	Engineering	Mathematics, 10/	e, John Wiley	/ & S	ons, 2011.		
2. B.V.Ramana.	"Higher Engi	neering Math	ematics", Mc Gra	w Hill publish	ers.			
3. Engineering N	Mathmatic I &	II, by T.K.V	. Iyengar, B.Krish	na Gandhi, S.	Ranga	anatham and		
M.V.S.S.N.Pras	ad S. Chand p	ublication.						

CHEMISTRY(Common to CSE,AI&ML,CS,ECE,EEE,DS)								
Course Code	L:T:P:S	Credits	Exam Marks	Exam Duration	Course Type			
22A0006T	3: 0:0:0	3	CIE: 30 SEE:70	3Hours	BS			
Course Objective	s: Student	t will be ab	le to					
 To familiarize engineering chemistry and its applications To train the students on the principles and applications of electrochemistry and polymers To introduce instrumental methods 								
			Syllabus		Total Hours:			
					48 Hrs			
Module- I		St	ructure and Bond	ing	9Hrs			
Planck's quar	ntumtheory	y,dualnature	ofmatter,Schroding	erwaveequation, sign	nificanceofY			
and Ψ^2 , mole molecules – en	cular orbit ergy level	tal theory diagrams of	- bonding in home f O ₂ and CO, etc. π	o- and heteronucle	ear diatomic of butadiene			
and benzene, ca	alculation	of bond ord	er.					
Module-II		Mode	rn Engineering ma	aterials	10Hrs			
doping on band Supercapacitor Nano chemist applications of	lstructures. s: Introduc ry: Intro Fullerenes,	tion, Basic duction, c andcarbon	concept-Classificat lassification of 1 nanotubes.	ion – Applications. nanomaterials, pro	perties and			
Module-III		Electro	chemistry and App	olications	10Hrs			
Electrodes – concepts, reference electrodes (Calomel electrode, Ag/AgCl electrode and glass electrode);Electrochemicalcell,Nernstequation,cellpotentialcalculationsandnumericalpro blems, potentiometry- potentiometric titrations (redox titrations), conductometric titrations (acid-base titrations). Primary cells: Zinc-air battery, Secondary cells: lead acid and lithium-ion batteries- working of the batteries including cell reactions, Fuel cells: hydrogen-oxygen, methanol								
Module-IV		<u> </u>	Polymer Chemistr	у	10Hrs			
Introduction to polymers, functionality of monomers, Types of polymerization-addition, condensation and copolymerization with specific examples and mechanisms of polymerization. Plastics - Thermoplastics and Thermosetting, Preparation, properties and applications of – PTFE, Bakelite, Calculation of molecular weight of polymer by weight average and number average method, Polydispersity Index. Elastomers–Buna-S, Buna-N–preparation, properties and applications. Conducting polymers – polyacetylene, polyaniline, – mechanism of conduction and applications. Biodegradable polymers: polylactic acid, poly dioxanone, starch, cellulose.								
Module-V	I	nstrumenta	al Methods and its	applications	9Hrs			

EMR spectra, Beer-Lambert's law, Basic Principle, Instrumentation and applications of UVvisible spectrophotometer and FTIR, Chromatography-Introduction, Principle and instrumentation of Gas Chromatography (GC), retention time, TLC, Rf factor.

Course Outcomes (CO): After completion of the course, students will be able to

- Describe Planck's quantum theory, dual nature of matter, Schrodinger equation, molecular orbital Theory and molecular orbital energy level diagram of different molecules
- Explain Crystal field theory, splitting in octahedral and tetrahedral geometry and the magnetic behaviour, Oxidation state, coordination and colour of complexes.
- Explain the principle of Band diagrams of conductors, superconductor, semiconductors and insulator and nonmaterial
- Discuss the principles of electrochemistry in potentiometry, conductometry, battery and electrochemical sensors
- Explain polymerization and the preparation, properties, and applications of thermoplastics & thermosetting, elastomers, & conducting polymers
- > Discuss the different applications of analytical instruments

Textbooks:

1. P. C. Jain & Monika Jain, Engineering Chemistry, Dhanpat Rai Publishing Company

(P) Ltd, New Delhi, 16th edition, 2013.

- 2. K. N. Jayaveera, G. V. Subba Reddy and C. Ramachandriah, Engineering Chemistry, Mc.Graw Hill Publishers, New Delhi.
- 3. Energy scenario beyond2100,by S.Muthu Krishna Iyer.

ReferenceBooks:

- J. D. Lee, Concise Inorganic Chemistry, Oxford University Press, 5th edition 2010.
- 2. Skoog and West, Principles of Instrumental Analysis, Thomson, 6th edition, 2007.
- 3. Peter Atkins, Julio de Paula and James Keelar, Atkins' Physical Chemistry, Oxford University Press, 10th edition, 2010.

FUNDAMENTALS OF ELECTRICAL CIRCUITS (common to EEE&ECE)								
Course Code	L:T:P:S	Credits Exam M	larks	Exam Duratio	n Cou	irse Type		
	3: 0:0:0	3 CIE: 30	SEE:70	3Hours	BS	J I		
Course Objective	es: Student v	vill be able to		I.	1			
1. Basic char	acteristics of	R, L, C parameter	s, their Vo	oltage and Curre	ent Relations	and		
Various co	ombinations of	of these parameters	5.	C				
2. Basics of l	Magnetic circ	cuits						
3. Network Topology and concepts like Tree, Cut-set, Tie-set, Loop, Co-Tree								
4. The Single	e Phase AC c	ircuits and concept	ts of real p	ower, reactive	power, compl	lex power,		
phase ang	le and phase	difference.						
5. Network t	heorems and	their applications						
Unit - I	In	troduction to Elec	ctrical Ci	rcuits	10	Hrs		
Electrical Circui	ts: Circuit Co	oncept – Types of	elements -	- Source Transfe	ormation-Vol	tage – Current		
Relationship for	Passive Eler	nents. Kirchhoff's	s Laws –	Network Redu	uction Techn	niques- Series,		
Parallel. Series	Parallel. Sta	r-to-Delta or Del	ta-to-Star	Transformatio	n. Nodal Ai	nalvsis. Mesh		
Analysis Exampl	es		tu to btui	11uno10111uno	i, 100001 11	inary 515, 1010511		
Learning Outcom	0.00							
	es.							
At the end of this	unit, the stud	lent will be able						
I. To know about	Kirchhoff's	Laws in solving se	ries, paral	lel, non-series-p	arallel config	gurations in		
DC networks								
2. To know about	voltage sour	ce to current sourc	e and vice	-versa transform	nation in their	r		
representation								
3. To understand	analysis of N	odal and Mesh ana	lysis for d	lifferent circuits				
Unit - II	In	troduction to Ma	onetic Ci	renits	8	Hrs		
Magnetic Circuits	: Faraday's I	aws of Electroma	onetic Ind	uction-Concept	of Self and M	Autual		
Inductance-Dot C	onvention-C	pefficient of Coupl	ing-Comr	osite Magnetic	Circuit-Analy	vsis of Series		
and Derallal Magr	onvention-e		ing-comp	Joshe Magnetie	Circuit-Anai	ysis of Series		
	lette Circuits							
Learning Outcom	es:							
At the end of this	unit, the stud	lent will be able to						
1.To understand F	Faraday's law	'S						
2.To distinguish a	nalogy betwo	een electric and ma	ignetic cir	cuits				
3. To understand	analysis of se	ries and parallel m	agnetic ci	rcuits.				
Unit - III		Graph th	eory		9	Hrs		
		•	v					
Definitions – Gray	ph – Tree, Ba	sic Cutset and Bas	ic Tieset I	Matrices for Pla	nar Networks	s – Loop and		
Nodal Methods of	f Analysis of	Networks & Indep	endent Vo	oltage and Curre	ent Sources, N	Network		
equilibrium equat	ions -Duality	& Dual Networks						
Learning Outcom	es:							
At the end of this unit, the student will be able								
1. To understand basic graph theory definitions which are required for solving electrical circuits								
2. To understand about loop current method								
3. To understand about nodal analysis methods								
4. To understand about principle of duality and dual networks								
p. 10 identify the solution methodology in solving electrical circuits based on the topology								
		Single Phase A.	<u>C Circuit</u>		<u> </u>	Hrs		
K.M.S, Average	values and Fo	orm Factor for Diff	erent Peri	odic wave For	ns – Sinusoic	al Alternating		
Quantities – Phase	e and Phase I	Series Density	nex and P	Demolial C	epresentation	is, Steady State		
marysis of K, I		series, Parallel a	nu series	s ratallet Com	omations) W	iui sillusoldal		

Excitation - Phasor diagrams - Concept of Power Factor- Concept of Reactance, Impedance, Susceptance and Admittance-Apparent Power, Active and Reactive Power, Examples. Resonance.

Learning Outcomes:

At the end of this unit, the student will be able

1. To understand fundamental definitions of $1-\phi$ AC circuits

2. To distinguish between scalar, vector and phasor quantities

3. To understand voltage, current and power relationships in 1- ϕ AC circuits with basic elements R, L, and C.

4. To understand the basic definitions of complex immittances and complex power

5. To solve $1-\phi$ AC circuits with series and parallel combinations of electrical circuit elements R, L and C.

Unit - V Network Theorems

10 Hrs

Superposition, Reciprocity, Thevenin's, Norton's, Maximum Power Transfer, Millmann's, Tellegen's, and Compensation Theorems for D.C and Sinusoidal Excitations. Learning Outcomes:

At the end of this unit, the student will be able

1. To know that electrical circuits are 'heart' of electrical engineering subjects and network theorems are main part of it.

2. To distinguish between various theorems and inter-relationship between various theorems

3. To know about applications of certain theorems to DC circuit analysis

4. To know about applications of certain theorems to AC network analysis

5. To know about applications of certain theorems to both DC and AC network analysis

Course Outcomes (CO): After completion of the course, students will be able to

- Explain types of networks and Network Reduction Techniques
- > Analyze Magnetic Circuits and Coupled circuits.
- > Analysis of electrical networks using graph theory and duality and dual networks
- > Analyze RLC circuits with AC Excitation
- > Analyze the power, voltage and current for different network configurations.
- > Apply theorems for finding the solutions of network problems

Textbooks:

1. Fundamentals of Electric Circuits Charles K. Alexander and Matthew. N. O. Sadiku, Mc Graw Hill, 5th Edition, 2013.

2. Engineering circuit analysis William Hayt and Jack E. Kemmerly, Mc Graw Hill Company, 7th Edition, 2006.

3. Circuit Theory Analysis & Synthesis A. Chakrabarti, Dhanpat Rai & Sons, 7th Revised Edition, 2018.

Reference Books:

1. Network Analysis M.E Van Valkenberg, Prentice Hall (India), 3rd Edition, 1999.

- 2. Electrical Engineering Fundamentals V. Del Toro, Prentice Hall International, 2nd Edition, 2019.
- 3. Electric Circuits- Schaum's Series, Mc Graw Hill, 5th Edition, 2010.

4. Electrical Circuit Theory and Technology John Bird, Routledge, Taylor & Francis, 5th Edition, 2014.

Electronic Devices and Circuits								
Course Code	Course CodeL:T:PCreditsExam. MarksExam DurationCourse Type							
22A0401T	3:0:0	3	CIE:30 SEE:70	3 Hours	PC			
22A0401T 3:0:0 3 CIE:30 SEE:70 3 Hours PC Course Objectives: • To understand the basic principles of all semiconductor devices. • To be able to solve problems related to diode circuits, and amplifier circuits. • To analyze diode circuits, various biasing and small signal equivalent circuits of amplifiers. • To be able to compare the performance of BJTs and MOSFETs. • To design rectifier circuits and various amplifier circuits using BJTs and MOSFETs. • To design rectifier circuits and various amplifier circuits using BJTs and MOSFETs. • To design rectifier circuits and various amplifier circuits using BJTs and MOSFETs. • To design rectifiers - Introduction, The Ideal Diode – current voltage characteristic, rectifier, diode logic gates, Terminal Characteristics of Junction Diodes– forward bias, reverse bias, and breakdown regions. Applications: Rectifiers – Half wave, Full wave rectifier and Bridge rectifier. Filters - Inductor, Capacitor, L-section and π -Filters, Zener Diodes– Zener diode Characteristics, Voltage shunt regulator, Diode as switch, Clipping and Clamping Circuits– limiter circuit, the clamped capacitor, voltage doubler, Special Diode Types– UJT, Schottky barrier diode, Varactor diode, photo diode, light emitting diode(LED), Problem Solving. Unit –II Bipolar Junction Transistors (BJTs): Physical Operation - simplified structure and modes of								
Characteristics- dependence of Configurations Base (CB) amp	of different cor collector curren - Common-Emi lifier, Common-	ifiguration ifiguration it on coll tter (CE) a Collector (s - graphical represent ector voltage, the Eat amplifier without and CC) amplifier or Emi	tation of transistor arly Effect, Basic with emitter resistat	characteristics, BJT Amplifier ance, Common- lem Solving.			
			Unit –III		em sorving.			
MOS Field-Effect Transistors (MOSFETs): Introduction, Device Structure and Physical Operation – device structure, operation with zero gate voltage, creating a channel for current flow, operation for different drain to source voltages, the P-channel MOSFET,CMOS, V-I characteristics– $i_D - v_{DS}$ characteristics, $i_D - v_{GS}$ characteristics, finite output resistance in saturation, characteristics of the p-Channel MOSFET, MOSFET Circuits at DC, Applying the MOSFET in Amplifier Design – voltage transfer characteristics, biasing the MOSFET to obtain linear amplification, the small signal voltage gain, graphical analysis, the Q-point. Problem solving.								
Dissing of DI	Pa & MOSFET	2a. Diagia	Unit –IV	a anarating point	fined biog calf			
Biasing of BJT's & MOSFET's: Biasing of BJT's – load line, operating point, fixed bias, self bias, voltage divider bias circuits, Bias compensation, Thermal runaway, condition for Thermal stability, Biasing of MOSFET's - Fixed bias, Self bias, Voltage divider bias circuits, Problem solving.								
MOSFET Small Signal Operation Models – the dc bias, separating the DC analysis and the signal analysis, Small signal equivalent circuit models, the transconductance, the T equivalent circuit model, Basic MOSFET Amplifier Configurations– three basic configurations, characterizing amplifiers, common source(CS) amplifier without and with source resistance, common gate (CG) amplifier, source follower, the amplifier frequency response, Problem solving.								
			30					

Text Books:

- Adel S. Sedra and Kenneth C. Smith, "Microelectronic Circuits Theory and Applications", 6th Edition, Oxford Press, 2013.
- 2. Donald A Neamen, "Electronic Circuits analysis and design", 3rd Edition, McGraw Hill (India), 2019.

References:

- 1. J. Milliman and C Halkias, "Integrated electronics", 2nd Edition, Tata McGraw Hill, 1991.
- 2. R.L. Boylestad and Louis Nashelsky, "Electronic Devices and Circuits," 9th Edition, Pearson, 2006.

Course Outcomes:

After the completion of the course students will able to

- 1. Understand principle of operation, characteristics and applications of Semiconductor diodes.
- 2. Design the diode applications such as rectifiers, clippers and clampers.
- 3. Understand principle of operation, characteristics and applications of Bipolar Junction Transistor and MOSFETs.
- 4. Design amplifiers using BJTs, and MOSFETs.
- 5. Solve the problems related to Semiconductor diodes, BJTs, and MOSFETs.
- 6. Analyze performance of diode applications, biasing circuits of BJTs, MOSFETs and their applications.

Chemistry Lab									
(Common to CSE,AI&ML,CS,ECE,EEE,DS)									
Course Code	L:T:P:S	Credits	Exam Marks	Exam Duratio	on Course Type				
22A0011P	0:0:1.5:0	1.5	CIE:30 SEE:70	3Н	BS				
This course wi	ll enable students	Con to:	urse Objectives:	stogethands-					
onexperienceontheprinciples discussed in theory sessions and to understand the applications of these concepts in engineering.									
		Syllabus			Total Hours: 48				
		List	of Experiments						
 Conduct metric titration of strong acid vs. strong base, Determination of cell constant and conductance of solutions Potentiometry - determination of redox potentials and emfs pH metric titration of strong acid vs. strong base Determination of Strength of an acid in Pb-Acid battery Preparation of a polymer Verification of Lambert-Beer's law Preparation of organic mixtures by Thin Layer chromatography Identification of Simple organic compounds by IR. Estimation of Ferrous Iron by Dichrometry. Determination of Copper by EDTA method. (Any 10 experiments from the above list) 									
Course Outcom	es:								
On completio Determ conduc	n of this course, th ine the cell cons tometry	he students ar tant and cond	e able to: ductance of solutio	ons and the stre	ngth of an acid by				
> Synthe	size of advanced I	polymer mate	rials						
 Measur volume 	the strength of the strength o	of an acid p	resent in secondar	y battery and	Ferrous ion using				
> Determ	ine the potentials	and EMFs of	solutions byPotent	iometry					
> Identify	y some organic an	d inorganic c	ompounds by instru	imental methods	5				
> Synthe	size of nanomater	ials by simple	e methods						
Text Book(s):									
 A Textbook of Quantitative Analysis, Arthur J. Vogel. Jain & Jain. Engineering Chemistry: Dhanapathrai Publications., 2015. S.S.Dara, Experiments and Calculations in Engineering Chemistry: S-Chand Publications, Revised edition, 2008. 									
1.	 S.K. Bhasin and Sudha Rani, "Laboratory Manual on Engineering Chemistry", Dhanpat Rai Publishing Company, New Delhi, 2nd edition. Sunitha Rattan, "Experiments in Applied Chemistry", S.K. Kataria& Sons, New Delhi, 2nd edition. 								
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FUNDAMENTALS OF ELECTRICAL CIRCUITS LABORATORY (Common to EEE & ECE)								
Course Code	L:T:P:S	Credits	Exam Marks	Exam Duration	Course Type			
22A0011P	0:0:3:0	1.5	CIE:30 SEE:70	3Н	ESC			
Course Obj	ectives:			I				
This course	will enable stu	idents to:	rious theorems a	nd varify practi	colly			
2. Understar	id and analyze	and appry val	ive power measu	rements in thre	e phase balanced & un			
balanced cir	cuits							
		Sy	llabus		Total Hours: 48			
			List of Experim	nents	·			
 Verificati Verificati Verificati Determination Determination Verificati 	on of Kirchho on of mesh an on of nodal an ation of averag hard ware beries and Para on of Series an on of Superpo n Power Trans tion of Compe- tion of Recipro- hation of Self,	ff's current la alysis using h alysis using h ge value, rms allel RLC circ ad Parallel Re n's and Norto sition Theore fer Theorem nsation Theo ocity, Millma Mutual Indu (Any 10 es	aw and voltage la nard ware and dig nard ware value, form fact cuits. esonance on's Theorems for DC and AC of rem for DC circu nn's Theorems f ctances and Coer	w using hard v gital simulation or, peak factor circuits nits for DC circuits fficient of Coup n the above lis	vare of sinusoidal wave, square pling t)			
Course Outco	omes:							
On comple	tion of this cou	urse, the stude	ents are able to:					
 Anal Anal Anal Anal Appl Appl Appl Anal 	 Analyze network parameters and types of networks Analyze RLC circuits and coupled circuits. Analyze Resonance for different circuits. Apply theorems for finding the solutions of network problems Apply Maximum power transfer theorems for finding the solutions of DC & AC Networks Analyze coupled circuits. 							
Text Book	s):		1 17 4 1	1 114 41				
1. Fundamer Hill, 5th Edi	tion. 2013.	ic Circuits Ch	iarles K. Alexano	aer and Matthe	w. N. O. Sadiku, Mc Graw			
2. Engineeri	2. Engineering circuit analysis William Hayt and Jack E. Kemmerly, Mc Graw Hill Company, 7th							
Edition, 2006.								
3. Circuit Theory Analysis & Synthesis A. Chakrabarti, Dhanpat Rai & Sons, 7th Revised Edition, 2018								
2010 Reference Book(s):								
1. Netw 2. 2. El Edition, 201 3. 3. El 4. 4 Fl	 Reference Book(s): Network Analysis M.E Van Valkenberg, Prentice Hall (India), 3rd Edition, 1999. 2. Electrical Engineering Fundamentals V. Del Toro, Prentice Hall International, 2nd Edition, 2019. 3. Electric Circuits- Schaum's Series, Mc Graw Hill, 5th Edition, 2010. 4. Electrical Circuit Theory and Taska along John Divid Developer Turken & Fundamental Structure of Taska along John Divid Developer Turken & Fundamental Structure (International Structure) 							

Edition, 2014.

ELECTRONIC DEVICES AND CIRCUITS LAB

Course Code	L:T:P	Credits	Exam.	Exam	Course Type
			Marks	Duration	
22A0402P	0:0:3	1.5	CIE:30	3 Hours	PC
			SEE:70		

Course Objectives:

- To verify the theoretical concepts practically from all the experiments.
- To analyse the characteristics of Diodes, BJT, MOSFET.
- To design the amplifier circuits from the given specifications.
- To Model the electronic circuits using tools such as PSPICE/Multisim.

Syllabus

LIST OF EXPERIMENTS: (Conduct all experiments).

Note: All the experiments shall be implemented using both Hardware and Software.

- 1. Design a half wave rectifier with and without filters for the given specifications, and verify the results experimentally for different load conditions, also Calculate ripple factor with relevant graphs.
- 2. Design a full wave rectifier with and without filters for the given specifications, and verify the results experimentally for different load conditions, also Calculate ripple factor with relevant graphs
- 3. Verify the operation of various clipping and clamper circuits using PN junction diode experimentally.
- 4. Design a voltage regulator using Zener diode and verify load regulation characteristics.
- 5. Analyze the input and output characteristics of BJT in Common Emitter configuration experimentally.
- 6. Analyze the input and output characteristics of BJT in Common Base configuration experimentally.
- 7. Design voltage- divider bias/self-bias circuit using BJT and verify experimentally.
- 8. Design a small signal amplifier using BJT (common emitter) for the given specifications also calculate Bandwidth.
- 9. Analyze the output and transfer characteristics of MOSFET in Common Source Configuration experimentally.
- 10. Design self-bias circuit using MOSFET and verify experimentally.
- 11. Verify the operation of a switch using CMOSFET/JFET/BJT experimentally.
- 12. Design a small signal amplifier using MOSFET (common source) for the given specifications also calculate Bandwidth.

Tools / Equipment Required: Software Tool like Multisim/ Pspice or Equivalent,

DC Power supplies, Multi meters, DC Ammeters, DC Voltmeters, AC Voltmeters, CROs, all the required active devices.

Course Outcomes:

After the completion of the course students will able to

- 1. Understand the operation and characteristics of basic electronic devices.
- 2. Design the Diode applications like Rectifiers, Clippers and Clampers for the given specifications.
- 3. Analyze the Characteristics of Diodes, BJTs, MOSFETs.
- 4. Design BJT based amplifiers for the given specifications.
- 5. Design MOSFET based amplifiers for the given specifications
- 6. Simulate Diode, BJT and MOSFET applications in PSPICE /Multisim.

ELECTRONICS WORKSHOP

Course Code	L:T:P	Credits	Exam.	Exam	Course Type
			Marks	Duration	
22A0403P	0:0:3	1.5	CIE:30	3 Hours	PC
			SEE:70		

Course Objectives:

- To introduce electronic components, measuring instruments and tools used in electronic workshop.
- To equip with the knowledge of understanding data sheets of electronic components.
- To give practical experience on soldering the electronic components on a PCB.
- To introduce EDA tools.
- To know about the internal parts of a computer, assembling a computer from the parts, preparing a computer for use by installing the operating system
- To provide training on Productivity tools like word processors, spreadsheets, presentations.

Syllabus

List of Exercises / Experiments:

1. Familiarization of commonly used Electronic Workshop Tools: Bread board, Solder, cables, relays, switches, connectors, fuses, Cutter, plier, screwdriver set, wire stripper, flux, knife/blade, soldering iron, de-soldering pump etc.

• Provide some exercises so that electronics hardware tools and instruments are learned to be used by the students

2. Familiarization of Electronic Measuring Instruments like Voltmeters, Ammeters, multimeter, LCR-Q meter, Power Supplies, CRO, DSO, Function Generator, Frequency counter.

• Provide some exercises so that electronic measuring instruments are learned to be used by the students

3. Electronic Components: Familiarization/Identification of electronic components (Resistors, Capacitors, Inductors, Diodes, transistors, IC's etc.) – Functionality, type, size, color coding, package, symbol, cost etc.

4. Testing of electronic components like Resistor, Capacitor, Diode, Transistor, ICs etc.

• Compare values of components like resistors, inductors, capacitors etc with the measured values by using electronic instruments

5. Study of Cathode Ray Oscilloscope (CRO)

- Find the Amplitude and Frequency of a signal
- Measure the Unknown Frequency & Phase difference of signals using Lissajous figures 6. Interpret data sheets of discrete components and IC's.
 - Write important specifications/ratings of components & ICs and submit it in the form of a report

7. Introduction to EDA Tools: MULTISIM/PSPICE/TINA schematic capture tool, learning of basic functions of creating a new project, getting and placing parts, connecting placed parts, simulating the schematic, plotting and analyzing the results.

Provide some exercise so that students are familiarized in using EDA tools

8. Assembling and Testing of simple electronic circuits on breadboards; identifying the components and its location on the PCB, soldering of the components, testing the assembled circuit for correct functionality.

Course Outcomes:

- Identify discrete components and ICs.
- Assemble simple electronic circuits over a PCB.
- Test various components.
- Interpret specifications (ratings) of the component.

IT WORKSHOP Course Code L:T:P Credits Exam. Marks Exam Duration Course 22A0502P 0:0:3 1.5 CIE:30 SEE:70 3 Hours ESC

Course Objectives:

- To make the students know about the internal parts of a computer, assembling and dissembling a computer from the parts, preparing a computer for use by installing the operating system
- To provide Technical training to the students on Productivity tools like Word processors Spreadsheets, Presentations and LAteX
- To learn about Networking of computers and use Internet facility for Browsing and Searching

Syllabus

Task 1: Learn about Computer: Identify the internal parts of a computer, and its peripherals. Represent the same in the form of diagrams including Block diagram of a computer. Write specifications for each part of a computer including peripherals and specification of Desktop computer. Submit it in the form of a report.

Task 2: Assembling a Computer: Disassemble and assemble the PC back to working condition. Students should be able to trouble shoot the computer and identify working and non-working parts. Student should identify the problem correctly by various methods

Task 3: Install Operating system: Student should install Linux on the computer. Student may install another operating system (including proprietary software) and make the system dual boot or multi boot. Students should record the entire installation process.

Task 4: Operating system features: Students should record the various features that are supported by the operating system(s) installed. They have to submit a report on it. Students should be able to access CD/DVD drives, write CD/DVDs, access pen drives, print files, etc. Students should install new application software and record the installation process. Networking and Internet

Task 5: Networking: Students should connect two computers directly using a cable or wireless connectivity and share information. Students should connect two or more computers using switch/hub and share information. Crimpling activity, logical configuration etc. should be done by the student. The entire process has to be documented.

Task 6: Browsing Internet: Student should access the Internet for Browsing. Students should search the Internet for required information. Students should be able to create e-mail account and send email. They should get acquaintance with applications like Facebook, skype etc. If Intranet mailing facility is available in the organization, then students should share the information using

it. If the operating system supports sending messages to multiple users (LINUX supports it) in the same network, then it should be done by the student. Students are expected to submit the information about different browsers available, their features, and search process using different natural languages, and creating email account.

Task 7: Antivirus: Students should download freely available Antivirus software, install it and use it to check for threats to the computer being used. Students should submit information about the features of the antivirus used, installation process, about virus definitions, virus engine etc. Productivity tools

Task 8: Word Processor: Students should be able to create documents using the word processor tool. Some of the tasks that are to be performed are inserting and deleting the characters, words and lines, Alignment of the lines, Inserting header and Footer, changing the font, changing the colour, including images and tables in the word file, making page setup, copy and paste block of text, images, tables, linking the images which are present in other directory, formatting paragraphs, spell checking, etc. Students should be able to prepare project cover pages, content sheet and chapter pages at the end of the task using the features studied. Students should submit a user manual of the word processor considered, Image Manipulation tools.

Task 9: Presentations: creating, opening, saving and running the presentations, selecting the style for slides, formatting the slides with different fonts, colours, creating charts and tables, inserting and deleting text, graphics and animations, bulleting and numbering, hyperlinking, running the slide show, setting the timing for slide show.

Task 10: Spreadsheet: Students should be able to create, open, save the application documents and format them as per the requirement. Some of the tasks that may be practiced are Managing the worksheet environment, creating cell data, inserting and deleting cell data, format cells, adjust the cell size, applying formulas and functions, preparing charts, sorting cells. Students should submit a user manual of the Spreadsheet

Task 11: LateX: Introduction to Latex and its installation and different IDEs. Creating first document using Latex, using content into sections using article and book class of LaTeX. Styling Pages: reviewing and customizing different paper sizes and formats. Formatting text (styles, size, alignment, colors and adding bullets and numbered items, inserting mathematical symbols, and images, etc.). Creating basic tables, adding simple and dashed borders, merging rows and columns. Referencing and Indexing: cross-referencing (refer to sections, table, images), bibliography (references).

References:

1. Introduction to Computers, Peter Norton, McGraw Hill

2. MOS study guide for word, Excel, Powerpoint& Outlook Exams, Joan Lambert, Joyce Cox, PHI.

Introduction to Information Technology, ITL Education Solutions limited, Pearson Education.
 Networking your computers and devices, Rusen, PHI

5. Trouble shooting, Maintaining & Repairing PCs, Bigelows, TMH

6. Lamport L. LATEX: a document preparation system: user's guide and reference manual. Addison-wesley; 1994.

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Basic Electrical and Electronics Engineering										
(Common for all branches excluding EEE & ECE)										
Course Code L:T:P Credits Exam. Marks Exam Duration Course Type										
22A0240P	0:0:3	1.5	CIE:30 SEE:70	3 Hours	PC					
Course Objectiv	es:									
To get practical k	nowledge abou	t basic electrica	l circuits, electro	nic devices like D	Diodes, BJT, JFET					
and also analyze t	he performance	e of DC Motors	, AC Motors and	Transformers.						
		~								
		Sy	llabus							
LIST OF EXPE	RIMENTS: (C	onduct all exp	eriments).		_					
Note: All the exp	eriments shall	be implement	ed using both H	ardware and Sof	itware.					
Equipment Req	uired:									
1. Verificatio	on of Kirchhoff	's Laws.								
2. Verificati	on of Superpos	ition Theorem.								
3. Magnetiz	ation characteri	stics of DC Sh	unt Generator.							
4. Brake Te	st on DC-Shunt	Motor. Determ	ination of Perfor	mance curves.						
5. OC & SC	Tests on Singl	e Phase Transfo	ormer.							
6. V-I Chara	icteristics of So	lar Cell								
7. V-I Chara	cteristics of PN	junction Diode								
8. V-I Chara	cteristics of Zei	her Diode	• •							
9. Half Way	e Rectifier and	Full Wave rect	ifier.							
10. Input and	Output charact	eristics of BJT	with CE configur	ration						
11. Input and	Output charact	eristics of BJ I	with CB configu	ration						
12. Input and		teristics of JFE	1.							
Additiona 12 Snood con	ii Experiments	Matan								
13. Speed con	trol of DC Shu	nt Motor	otor							
14. Brake re	st on Three Pha	se induction M	otor.							
Course Outcome	s:									
After the complet	ion of the cours	se students will	able to,							
			,							
1. Experiment	ntally verify the	basic circuit th	eorems, KCL an	d KVL						
2. Draw the	Open circuit ch	aracteristics of	DC Shunt Genera	ator circuits exper	rimentally.					
3. Acquire h	ands on experie	ence of conducti	ing various tests	on dc shunt motor	r, single phase					
transforme	ers obtaining th	eir performance	e indices using st	andard analytical	as well as					
graphical	methods	-	2	-						
4. Experiment	ntally verify the	V-I characteris	stics of Solar cell							
5. Draw the	characteristics of	of different sem	iconductor devic	es like PN junctio	on Diode, Zener					
Diode, BJT and JFET by conducting suitable experiments.										

6. Experimentally verify the working of half and full wave rectifier by using PN Junction diodes