

**Faculty Contributions:**

**Awards/Publications/Conferences:**

- Mr.Naveen.Kolla Asst.Prof,is disgnated as “Committee Member” in IEEE Ananthapur,Subsection for student activities for the AY 2017-18 by IEEE Hyderabad.
- Mrs.K.Radhika, published a paper “Satellite Image Classification based on Ensemble Subspace Discriminant method using Random Subspace Algorithm” Helix Journal and will be published in Volume 8, Issue 1, 2018, 2714- 2718, January 2018.
- Mrs.K.Radhika, presented a paper “Multi class classification of satellite images”, in IEEE–International Conference on Innovative Mechanisms for Industry Applications (ICIMIA 2017), 20th, February 2017.
- Mrs.K.Radhika presented a paper “Multi spectral classification using cluster ensemble and self learning”, in “International Conference on Innovative Research in Emerging technologies (ICIRET-2017)”, 30th-31st, January 2017.

**Workshop/Seminar/Fdp Attended:**

- Mr. K. Prabhakar Reddy,Asst.Professor of ECE attended a three day Faculty Development Program on Signal Processing with Simulink conducted by Mathworks Training Services at Sree Vidyanikethan Engineering College, Tirupati from 7th to 9th February 2018
- A three day workshop on Embedded Systems was organized by the department of ECE, from 2<sup>nd</sup> to 04<sup>th</sup> February 2018in collaboration with APSSDC.
- Dr. Shaik. Mahaboob Basha attended a FDP on Advanced Trends in Signal Processing Applications in Eswar College of Engineering, Narasaraopet from 29-1-2018 to 3-2-2018.
- Ms. K. Radhika registered for NPTEL certification Course on Introduction to IoT for 12 weeks from Jan-April 2018.
- Ms. K. Radhika registered for NPTEL certification Course on MATLAB Programming for Numerical Computation for 8 weeks from Feb-June, 2018.
- Mr. Naveen Kolla attended a National Technical Seminar on Future Technologies for Combat Vehicle Electronics conducted by Combact Vehicles Research & Development Establishment (CVRDE), Chennai on 23-02-2018.

**Events Organized:**

**THREE DAY WORKSHOP ON EMBEDDED SYSTEMS USING IOT:**

Department of ECE in association with APSSDC, Dept. of Skill Development, and Entrepreneurship & Innovation Govt. of Andhra Pradesh conducted a three day workshop on ELECTRONICS INNOVATION ZONE – EMBEDDED SYSTEMS USING IOT during 2nd to 4th February 2018.



**ONE DAY INDUSTRIAL VISIT TO SHAR:**

It gave us immense pleasure in visiting SATISH DAWN SPACE CENTER (SDSC) SHAR, SRIHARIKOTA, NELLORE Distt on 01-02-2018. SDSC SHAR is one of India’s primary space center subordinated to Vikram Sarabhai Space Center Indian Research Organization –department of Space, founded by Government of India and involved in the launch complexes provide complete support for vehicle assembly, fuelling, checkout and launch operations. Apart from these, it has facilities for launching sounding rockets meant for studying the earth’s atmosphere.



**Student Achievements:**

**Workshops/Online Courses Attended:**

- Ms. V.Adbhutha Teja of III B.Tech ECE secured I prize in ELOCUTION competition at “SPSR Nellore District Level NSS Youth Festival” at GIST, Nellore and on 5th January 2018.
- Ms.Sk.Arshiya Tabassum of III B.Tech,ECE awarded 1st prize in “Essay writing” competition on “ELECTROLL REFORMS IN INDIA AND THE IMPACT OF CASTE, RELIGION AND CURB ILLEGAL ACTIVITIES” on the occasion of

National Voters Day celebrations organized by ELECTION COMMISSION OF INDIA at Vikrama Simhapuri University, Nellore on 25th January 2018.

- Ms.G.Divya Reddy of III B.Tech,ECE awarded 2nd prize in “Essay writing” competition on ELECTROLL REFORMS IN INDIA AND THE IMPACT OF CASTE, RELIGION AND CURB ILLEGAL ACTIVITIES” on the occasion of National Voters Day celebrations organized by ELECTION COMMISSION OF INDIA at Vikrama Simhapuri University, Nellore on 25th January 2018.
- The project titled “DESIGN AND DEVELOPMENT OF DATALOGGER FOR LIVE TRACKING OF VEHICLES “designed by Ms.S D Mohana Priya of IV ECE, Ms.G Divya Reddy, Ms.Shaik Arshiya Tabassum of III ECE is shortlisted for State Level Project Exhibition to held in Amaravathi, Vijayawada.
- The project titled “ALL DIGITAL ONCHIP PROCESS SENSOR USING PARTITIONED INVERTER BASED RING OSCILLATOR “designed by Mr.G.Rajesh,G.Guru Prakash,Mr.M.Yachendra of IV ECE, is shortlisted for second level in 2018 innovate FPGA Global Design Contest organized by INTEL and Terassic.
- The project titled “LOW POWER DIGITAL SIGNAL PROCESSOR ARCHITECTURE FOR WIRELESS SENSOR NODES “designed by Mr.P.kishore,Mr.Y.Naveen kumar Reddy,Mr.D.Kesava Kumar of IV ECE, is shortlisted for second level in 2018 innovate FPGA Global Design Contest organized by INTEL and Terassic.
- The project titled “DESIGN OF 64 BIT MULTIPLIER USING CARRY SAVE ADDER “designed by Ms.P.Sai Lahari,Ms.Sk.Bebejan,Ms.S.Sai Bhavya of IV ECE, is shortlisted for second level in 2018 innovate FPGA Global Design Contest organized by INTEL and Terassic.
- The project titled “A FAULT TOLERANCE TECHNIC FOR COMBINATIONAL CIRCUITS BASED ON SELECTIVE TRANSISTOR REDUNDANCY “designed by Ms.G.Veena Reddy,Ms.S.V.Sravani,Ms.G.Palguni of IV ECE, is shortlisted for second level in 2018 innovate FPGA Global Design Contest organized by INTEL and Terassic.
- The project titled “DESIGN OF A 64 BIT SQUARE ROOT CARRY SELECT ADDER USING BRENTKUNG ADDER AND BEC “designed by Mr.K.Himakar,Mr.J.Bhargav Sai,Mr.K.Surendra of IV ECE is shortlisted for second level in 2018 innovate FPGA Global Design Contest organized by INTEL and Terassic.

**Editorial Board: Editors: 1. Mr.M. Sivakrishna, 2. Mrs.C.Durga Tejaswi,**  
**Student Members: 1.Shaik Mahammad (III ECE), 2. Thurimerla Srikanth (III ECE)**  
**3. .M. Sudivya (II ECE), 4. N. Saiteja (II ECE).**



GEETHANJALI INSTITUTE OF SCIENCE AND TECHNOLOGY  
 (Approved by AICTE, New Delhi & Affiliated to JNTU, Anantapur)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

# NEWS LETTER

## ABSOLUTE ELECTRONICS

“Key to Success”

### Vision

To become a reputed learning centre producing competent professionals.

### Mission

- Provide Quality education through interactive teaching-learning practices.
- Establish Technology-enabled environment for core competencies including robotics.
- Arrange Industry-Interaction to hone professional skills.
- Organize activities to foster social skills and ethical values.

### WIRELESSTECHNOLOGY:

Wireless communication — otherwise known as “over the air” — is the transfer of information or power between two or more points that are not connected by an electrical conductor. The most common wireless technologies use radio waves. With radio waves, intended distances can be short, such as a few meters for Bluetooth or as far as millions of kilometers for deep-space radio communications. It encompasses various types of fixed, mobile, and portable applications, including two-way radios, cellular telephones, personal digital assistants (PDAs), and wireless networking.



### PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

- Apply Engineering concepts to solve Electronics and Communication Engineering problems of social relevance.
- Design and develop Electronic devices and Systems for Industry or pursue research.
- Demonstrate competencies through continuous learning and adapt to multi-disciplinary environment.
- Practice professional values and contribute to the societal needs.

### PROGRAM SPECIFIC OUTCOMES (PSOs)

- Professional Skills: Apply principles of Analog and Digital Electronics, Communication Systems, Image processing, VLSI and Embedded Systems to solve diverse problems.
- Software Knowledge: Develop solutions for complex engineering problems of social relevance by employing Xilinx, CC Studio, Micro Wind, Keil, NG Spice, Scilab tools.