

Faculty Contributions:**Publications:**

- ✦ Mrs.K.Radhika, “Multi spectral classification using cluster ensemble technique”, International Journal of Intelligent Systems Technologies and Applications (IJISTA), Volume 17, Issue 1/2, 55-69, April 2018.
- ✦ Mrs.K.Radhika, “A neural network based classification of satellite images for change detection applications” Cogent Engineering, volume 5, Issue 1, 2018, 1-9, June 2018.
- ✦ Dr. A. Rajan published a paper “Classification of intravascular ultrasound images based on nonnegative matrix factorization features and maximum likelihood classifier” in International journal of advances in signal and image sciences 2457-0370 Vol. 04 / June 2018 Scopus.
- ✦ Mr. M. Mahesh Kumar published a paper “Design of HighSpeed Multiplier by Using Carry Select Adder” in International Journal of Advanced Research, Ideas and Innovations in Technology (IJARIIT) 2454-132X Vol. 04 / June 2018.
- ✦ Mr. P. Raghava Reddy published a paper “Low power and area efficient shift registers using pulsed latches in International Journal of Advanced Research, Ideas and Innovations in Technology (IJARIIT) 2454-132X Vol. 04 / June 2018.
- ✦ Mr. U. Sreenivasulu published a paper “IoT Based Irrigation System By Using Distributed Wireless Network” in International Journal of Scientific Engineering and technology Research (IJSETR) 2319-8885 Vol. 7 / June 2018.
- ✦ Ms. D. Nagajyothi published a paper “DACA: The Solution For Future Demands” in International Journal of Current Advanced Research (IJCAR) 2319-6475 Vol. 07 / 2018.
- ✦ Ms. D.Nagajyothi published a paper “Face Recognition under Non-Uniform Motion Blur, Illumination and Pose Variations “in International Journal of Current Advanced Research (IJCAR) 2319-6475 Vol. 07 / 2018.
- ✦ Ms. K. Radhika published a paper “Multispectral Image Classification Using Cluster Ensemble Technique “ in International Journal of Intelligent Systems Technologies and Applications (IJISTA) 1740-8873 vol. 17 / May 2018 .
- ✦ Ms. K. Radhika published a paper “PAPR Reduction for OFDM based Massive MIMO Systems using Selected Mapping Technique “ in Asian Journal of Science and Technology (AJST) 0976- 3376 Vol. 09 / May 2018 .
- ✦ Ms. K. Radhika published a paper “A Neural Network Based Classification of Satellite Images for Change Detection Applications “Cogent Engineering 2331-1916 Vol. 05 / June 2018.
- ✦ Ms. M. Suhasini published a paper “An Advanced Garbage Monitoring System Using IoT in International Journal of Advanced Technology and Innovative Research (IJATIR) 2348-2370 Vol.10 / June 2018.
- ✦ Ms. Sk. M. Shahina published a paper “A Review upon Fast Discrete Curvelet Transforms in Satellite Image Processing in International journal of current advanced research 2319-6505 Vol. 07 / May 2018.
- ✦ Ms. Sk.M. Shahina published a paper “A Robust Approach for Multispectral Image Classification” in International Journal of Engineering and Technology (IJET) 2227-524X Vol. 07 / 2018.

Workshop/Fdp Attended:

- ✦ Mr. P. Raghava Reddy Attended a FDP on Computer Vision and Image Processing in Narayana Engg College, Gudur from 23.06.2018 to 29.06.2018.
- ✦ Mr. P. Raghava Reddy Attended a FDP on Trends in speech and language processing in relevance to telecommunication and machine learning in Narayana Engg College, Nellore from 04.06.2018 to 09.06.2018.
- ✦ Mr. S.Sreenivasulu, Attended a FDP on Trends in speech and language processing in relevance to telecommunication and machine learning in Narayana Engg College, Nellore from 04.06.2018 to 09.06.2018.
- ✦ Mr. M. Sivakrishna attended a workshop on Introduction to Robotics organized by SVCE, Tirupathi from 28.06.2018 to 29.06.2018.
- ✦ Mr. M. Sivakrishna attended a workshop on Embedded Systems design using TIVA in JNTUATexas Instruments in collaboration with Sapience Consulting Services Anathapur from 19.04.2018 to 20.04.2018.
- ✦ Mr. P. Raghava Reddy attended a workshop on Introduction to Robotics organized by SVCE, Tirupathi from 28.06.2018 to 29.06.2018.
- ✦ Mr. P. Raghava Reddy attended a Three day hands on work shop on “Artificial Intelligence and Deep Learning” organized by Madanapalle Inst of Tech .&Sci, Chittoor from 21.06.2018 to 23.06.2018.

Events Organized:

A Five Day Hands on Training in VLSI USING MICROWIND TOOL is organized for the department of ECE students in association with Embedded Wngs, Visakhapatnam from 09-04-2018 to 13-04-2018.

Student Achievements:

- ✦ Ms. G. Divya Reddy, Ms. Sk. Arshiya Tabassum of III B.Tech,ECE awarded 1st prize in the event “TechXtreme” in National Level Technical Symposium, SIGMOID 2K17 organised by Sri Venkateswara University College of Engineering, Tirupathi held on 8th and 9th April 2018.

- ✦ Mr. R.V.S.Narayana Kumar & P.Amrutha Varshini of III B.Tech,ECE awarded 1st prize in the event “ELECTRONICS-360” in National Level Technical Symposium, SIGMOID 2K17 organised by Sri Venkateswara University College of Engineering, Tirupathi held on 8th and 9th April 2018.
- ✦ Ms. V.Niharika & B.Sai Sushmitha of III B.Tech,ECE awarded 2nd prize in the event “CIRCUIT CITY “in National Level Technical Symposium, SIGMOID 2K17 organised by Sri Venkateswara University College of Engineering, Tirupathi held on 8th and 9th April 2018.
- ✦ The project titled “DESIGN AND DEVELOPMENT OF DATALOGGER FOR LIVE TRACKING OF VEHICLES “designed by Ms.S D Mohana Priya of IV ECE, Ms.G Divya Reddy, Ms.Shaik Arshiya Tabassum of III ECE is shortlisted for State Level Project Exhibition to held in Amaravathi,Vijayawada.
- ✦ The project titled “LOW POWER DIGITAL SIGNAL PROCESSOR ARCHITECTURE FOR WIRELESS SENSOR NODES “designed by Mr.P.kishore,Mr.Y.Naveen kumar Reddy,Mr.D.Kesava Kumar of IV ECE, is shortlisted for second level in 2018 innovate FPGA Global Design Contest organized by INTEL and Terassic.
- ✦ The project titled “DESIGN OF 64 BIT MULTIPLIER USING CARRY SAVE ADDER “designed by Ms.P.Sai Lahari,Ms.Sk.Bebejan,Ms.S.Sai Bhavya of IV ECE, is shortlisted for second level in 2018 innovate FPGA Global Design Contest organized by INTEL and Terassic.
- ✦ The project titled “A FAULT TOLERANCE TECHNIC FOR COMBINATIONAL CIRCUITS BASED ON SELECTIVE TRANSISTOR REDUNDANCY “designed by Ms.G.Veena Reddy,Ms.J.V.Sravani,Ms.G.Palguni of IV ECE, is shortlisted for second level in 2018 innovate FPGA Global Design Contest organized by INTEL and Terassic.
- ✦ Ms.V.Nandini, Mr. P. Kalyan Reddy of IV B.Tech, ECE presented a paper titled “VEHICLE TRAFFIC MONITORING USING CAMERA AND EMBEDDED SYSTEMS” in the National Level Technical Symposium, SIGMOID 2K17,organised by Sri Venkateswara University College of Engineering, Tirupathi held on 8th and 9th April 2018.
- ✦ Ms. Sk. Arifa of IV B.Tech, ECE presented a paper titled “5G WIRELESS TECHNOLOGY” in the National Level Technical Symposium, SIGMOID 2K17,organised by Sri Venkateswara University College of Engineering, Tirupathi held on 8th and 9th April 2018.
- ✦ Ms.N.Yamini, Ms.G.Lishitha of III B.Tech,ECE presented a paper titled “AUGMENTED REALITY” in the National Level Technical Symposium, SIGMOID 2K17,organised by Sri Venkateswara University College of Engineering, Tirupathi held on 8th and 9th April 2018.
- ✦ Mr.Sk.Mohammad of III B.Tech,ECE presented a paper titled “BRAINCHIP USING ELECTRODE ARRAY” in the National Level Technical Symposium, SIGMOID 2K17,organised by Sri Venkateswara University
- ✦ Ms.U.Likhitha of III B.Tech,ECE presented a paper titled “SOLAR MOBILE CHARGER” in the National Level Technical Symposium, SIGMOID 2K17,organised by Sri Venkateswara University College of Engineering, Tirupathi held on 8th and 9th April 2018.

Editorial Board: Editors: 1. Mr.M. Sivakrishna, 2. Mrs.C.Durga Tejaswi,
Student Members: 1.Shaik Mahammad (III ECE), 2. Thurimerla Srikanth (III ECE)
3. .M. Sudivya (II ECE), 4. N. Saiteja (II ECE).



GEETHANJALI INSTITUTE OF SCIENCE AND TECHNOLOGY
 (Approved by AICTE, New Delhi & Affiliated to JNTU, Anantapur)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

NEWS LETTER

ABSOLUTE ELECTRONICS

“Key to Success”

Vision

To become a reputed learning centre producing competent professionals.

Mission

- ✦ Provide Quality education through interactive teaching-learning practices.
- ✦ Establish Technology-enabled environment for core competencies including robotics.
- ✦ Arrange Industry-Interaction to hone professional skills.
- ✦ Organize activities to foster social skills and ethical values.

PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

- ✦ Apply Engineering concepts to solve Electronics and Communication Engineering problems of social relevance.
- ✦ Design and develop Electronic devices and Systems for Industry or pursue research.
- ✦ Demonstrate competencies through continuous learning and adapt to multi-disciplinary environment.
- ✦ Practice professional values and contribute to the societal needs.

PROGRAM SPECIFIC OUTCOMES (PSOs)

- ✦ Professional Skills: Apply principles of Analog and Digital Electronics, Communication Systems, Image processing, VLSI and Embedded Systems to solve diverse problems.
- ✦ Software Knowledge: Develop solutions for complex engineering problems of social relevance by employing Xilinx, CC Studio, Micro Wind, Keil, NG Spice, Scilab tools.

5G TECHNOLOGY

5G is the fifth generation wireless technology for digital cellular networks.As with previous standards, the covered areas are divided into regions called "cells", serviced by individual antennas. Virtually every major telecommunication service provider in the developed world is deploying antennas or intends to deploy them soon. The frequency spectrum of 5G is divided into millimeter waves, mid-band and low-band. Low-band uses a similar frequency range as the predecessor, 4G.

