

**Faculty Achievements:**

- Mr.K.Naveen kola participated in “**IEEE Section student congress**”conducted by **HCU, Hyderabad** from 3<sup>rd</sup>\_4<sup>th</sup> OCT-2015
- Mr.K.Naveen kolla participated in “**IEEE Student branch leadership training**”conducted by HCU, Hyderabad from 4th OCT-2015
- Mr.K.Naveen kolla participated in “**Engineering Education&Research seminar**”conducted by National Instruments India,Tirupathi. from 8thOCT-2015
- Mr.M.Krishna Prasad, Mr.P.Raghava Reddy, Mrs.G.Kameswari, Mr.V.Bhaskar Rao Mr.P.V.Krishna Rao, Mr.T.Venkata Sridhar, Mrs.D.Naga Jyothi, Ms.Sk. Shahina, Ms.G.Meenakshi, Mr.SK.Khajavali, Mr.K.Prabhakar Reddy, Mr.G.Kiran Kumar, Mr.M.Mahesh Kumar, Mr.M.Siva Krishna, Mr.Y.Mallikarjuna, Mr.S.Srinivasulu, Mr.T.Chiranjeevi, Mr.K.Naveen, participated in “**Engineering physics**”conducted by GIST,Nellore, Conducted by IIT,Bombayfrom 8th\_18thDEC-2015
- Mr.M.Krishna, Mr.P.Raghava Reddy, G.Kameswari, Mr.V.Bhaskar Rao, Mr.G.Kiran Kumar, Mr.T.Venkata Sridhar, Mrs.D.Naga Jyothi Prasad kola participated in “**Engineering STTP On Technical Communication**”conducted by GIST,Nellore, Conducted by IIT,Bombay.
- Mr.M.Mahesh Kumar, Mr.M.Mahesh Kumar, Mr.M.Mahesh Kumar, participated in “**Analog and Digital CMOS IC Design Flow Using Mentor Graphics EDA Tool**”conducted by NEC, Nellore from 29<sup>th</sup> and 30<sup>th</sup> DEC-2015.

**Department Events:****Industrial Visits**

Department of ECE, GIST, organized an Industrial Visit for All the IV B.Tech students to SHATISH DAWN SPACE CENTER (SDSC) SHAR, SRIHARIKOTA, NELLORE , on 15-10-2015 accompanied by Mr. P. Krishna Rao, Asst Professor, Mr. Mahesh Kumar, Asst Professor and Mrs. Masthan –e –

Shahina Shaik, Asst Professor. Visitors (students and the accompanying staff total 160 students) travelled to SDSC SHAR by transportation arranged by the GIST Student Branch. The journey commenced at around 4:00 AM pm from College Campus and reached SDSC SHAR at around 8:00am on 15/10/2015.

**Micro Strip Antenna Design & Applications**

A guest lecture on “Micro Strip Antenna Design & Applications” was organized by the Department of ECE, on 10-10-2015, at New Seminar Hall. Speaker of this lecture was Prof. C. R. Sarma, Adjunct, Member – IEEE. The Resource person initiated his speech highlighting the need of studying wireless communication systems; he also added the study is incomplete without an understanding of the operation and fabrication of Antennas. The Lecture started with the introduction of Micro-strip Antennas or simply Printed Antennas.

**Student Achievements:**

- A.Sravani(142U1A0401) and S.Keerthana(142U1A0499)have participated in the event of paper presentation at the MOHAN MANTRA organized by SVNE,TIRUPATHI on 16th to 18thOCT 2015.

Nanoelectronics is defined as nanotechnology which allows the integration of purely electronic devices, electronic chips and circuits. The digital systems are combined with analog/RF circuits. This type of technology fusion can be described as the 'More than Moore' domain of development. The nanoscale dimensions of nanoelectronic components for systems of giga-scale complexity measured on a chip or in a package. This scaling feature and the road to giga-scale systems can be described as the 'More Moore' domain of development. Nanotechnology improve the capabilities of electronic components by reducing the size of transistors used in integrated circuits, researchers are developing a type of memory chip with a projected density of one terabyte of memory per square inch and this increases the density of memory chips, by improving display screens on electronics devices and this reduces power consumption and also the weight and thickness of the screens.

Editorial Board:Faculty Members:

1. M. Sivakrishna, Asst. Professor,
2. Sk.Khajavali, Asst. Professor

Student Members:

1. G.Prasanth(III ECE),
2. SK.Karimulla(III ECE),
3. P. Saideepika (II ECE),
4. M. Vamsi (II ECE)



NEWS LETTER  
**ABSOLUTE ELECTRONICS**  
"Key to Success"

Vision

To become a reputed learning centre producing competent professionals.

Mission

- Provide Quality education through interactive teaching-learning practices.
- Establish Technology-enabled environment for core competencies including robotics.
- Arrange Industry-Interaction to hone professional skills.
- Organize activities to foster social skills and ethical values.

PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

Graduates of B. Tech in Electronics and Communication Engineering Programme shall be able to

- Apply Engineering concepts to solve Electronics and Communication Engineering problems of social relevance.
- Design and develop Electronic devices and Systems for Industry or pursue research.
- Demonstrate competencies through continuous learning and adapt to multi-disciplinary environment.
- Practice professional values and contribute to the societal needs.

PROGRAM SPECIFIC OUTCOMES (PSOs)

At the time of graduation, student of B.Tech in Electronics and Communication Engineering Programme shall be able to

- Professional Skills: Apply principles of Analog and Digital Electronics, Communication Systems, Image processing, VLSI and Embedded Systems to solve diverse problems.
- Software Knowledge: Develop solutions for complex engineering problems of social relevance by employing Xilinx, CC Studio, Micro Wind, Keil, NG Spice, Scilab tools.