

RF Integrated Circuits & VLSI Design Theory Online Classes Details Month of April & May- K.CHANDRA SEKHAR

S.No.	Year/Sem	Subject	Date of Lecture & Class timings	Topic	App Used	Link (if any Resources)
1	IV-Yr & II-Sem	RFIC	01-04-2020, 8.40 to 10.30	Noise figure LNA Design	Cisco Webex	
2	III-Yr & II-Sem	VLSI Design	06-04-2020, 8.40 to 09.35	Switch logic, Alternate gate logic	Cisco Webex	
3	IV-Yr & II-Sem	RFIC	06-04-2020, 10.50 to 12.40	power vs noise match	Cisco Webex	
4	IV-Yr & II-Sem	RFIC	07-04-2020, 8.40 to 10.30	Large signal Performance	Cisco Webex	
5	III-Yr & II-Sem	VLSI Design	07-04-2020, 11.45 to 12.40	floorplanning, placement	Cisco Webex	
6	IV-Yr & II-Sem	RFIC	08-04-2020, 8.40 to 10.30	Multipled based mixers	Cisco Webex	
7	III-Yr & II-Sem	VLSI Design	09-04-2020, 2.15 to 3.10	routing	Cisco Webex	
8	III-Yr & II-Sem	VLSI Design	13-04-2020, 8.40 to 09.35	power delay estimation	Cisco Webex	
9	IV-Yr & II-Sem	RFIC	13-04-2020, 10.50 to 12.40	mixer design, subsampling mixers	Cisco Webex	
10	IV-Yr & II-Sem	RFIC	14-04-2020, 8.40 to 10.30	Class A,AB Amplifier	Cisco Webex	
11	III-Yr & II-Sem	VLSI Design	14-04-2020, 11.45 to 12.40	Clock & power routing	Cisco Webex	
12	IV-Yr & II-Sem	RFIC	15-04-2020, 8.40 to 10.30	Class B,C Amplifier	Cisco Webex	
13	III-Yr & II-Sem	VLSI Design	16-04-2020, 2.15 to 3.10	VHDL Synthesis, Design Flow	Cisco Webex	
14	III-Yr & II-Sem	VLSI Design	20-04-2020, 8.40 to 09.35	Circuit Synthesis	Cisco Webex	
15	IV-Yr & II-Sem	RFIC	20-04-2020, 10.50 to 12.40	Class D,E,F Amplifier	Cisco Webex	
16	IV-Yr & II-Sem	RFIC	21-04-2020, 8.40 to 10.30	RF power amplifier design	Cisco Webex	
17	III-Yr & II-Sem	VLSI Design	21-04-2020, 11.45 to 12.40	Simulation	Cisco Webex	
18	IV-Yr & II-Sem	RFIC	22-04-2020, 8.40 to 10.30	VCO, Resonator	Cisco Webex	
19	III-Yr & II-Sem	VLSI Design	23-04-2020, 2.15 to 3.10	Design Capture tool , Design Verification Tool	Cisco Webex	
20	III-Yr & II-Sem	VLSI Design	27-04-2020, 8.40 to 09.35	Fault modeling,test generation	Cisco Webex	
21	IV-Yr & II-Sem	RFIC	27-04-2020, 10.50 to 12.40	Negative Resistance oscillator, PLL	Cisco Webex	
22	IV-Yr & II-Sem	RFIC	28-04-2020, 8.40 to 10.30	Phase Detectors ,Loop Filters	Cisco Webex	
23	III-Yr & II-Sem	VLSI Design	28-04-2020, 11.45 to 12.40	Design for testability BIST	Cisco Webex	
24	IV-Yr & II-Sem	RFIC	29-04-2020, 8.40 to 10.30	Frequency Synthesis	Cisco Webex	
25	III-Yr & II-Sem	VLSI Design	29-04-2020, 2.15 to 3.10	Gate Arrays,CPLD's,FPGA,s	Cisco Webex	
26	III-Yr & II-Sem	VLSI Design	01-05-2020, 08:30 to 9.30	Shifters	Cisco Webex	
27	IV-Yr & II-Sem	RFIC	04-05-2020, 08:30 to 10.30	requency synthesis	Cisco Webex	

28	III-Yr & II-Sem	VLSI Design	04-05-2020, 10:50 to 11.50	adders, ALU's	Cisco Webex	
29	IV-Yr & II-Sem	RFIC	05-05-2020, 10:50 to 12.50	oscillators	Cisco Webex	
30	IV-Yr & II-Sem	RFIC	06-05-2020, 08:30 to 10.50	oscillators	Cisco Webex	
31	III-Yr & II-Sem	VLSI Design	06-05-2020, 11:50 to 12.50	multifiers	Cisco Webex	
32	III-Yr & II-Sem	VLSI Design	07-05-2020, 11:50 to 12.50	parity generators	Cisco Webex	
33	III-Yr & II-Sem	VLSI Design	08-05-2020, 08:30 to 9.30	comparator	Cisco Webex	
34	III-Yr & II-Sem	VLSI Design	08-05-2020, 11:50 to 12.50	counters	Cisco Webex	
35	IV-Yr & II-Sem	RFIC	11-05-2020, 08:30 to 10.30	frequency divisipn	Cisco Webex	
36	III-Yr & II-Sem	VLSI Design	11-05-2020 , 10:50 to 11.50	high density memory elements	Cisco Webex	
37	IV-Yr & II-Sem	RFIC	12-05-2020, 10:50 to 12.50	integer-N-synthesis	Cisco Webex	
38	IV-Yr & II-Sem	RFIC	13-05-2020, 08:30 to 10.30	ractional synthesis	Cisco Webex	
39	III-Yr & II-Sem	VLSI Design	13-05-2020, 11:50 to 12.50	Full custom designs	Cisco Webex	
40	III-Yr & II-Sem	VLSI Design	14-05-2020, 11:50 to 12.50	semi costum designs	Cisco Webex	
41	III-Yr & II-Sem	VLSI Design	15-05-2020, 08:30 to 9.30	standard cell design	Cisco Webex	
42	IV-Yr & II-Sem	RFIC	18-05-2020, 08:30 to 10.30	phase Noise	Cisco Webex	
43	III-Yr & II-Sem	VLSI Design	18-05-2020, 10:50 to 11.50	Gate arrays	Cisco Webex	
44	IV-Yr & II-Sem	RFIC	19-05-2020, 10:50 to 12.50	Noise General Considerations	Cisco Webex	
45	IV-Yr & II-Sem	RFIC	23-05-2020, 08:30 to 10.30	Radio Architecture	Cisco Webex	
46	III-Yr & II-Sem	VLSI Design	23-05-2020, 10:50 to 11.50	CPLD's, FPGA,s	Cisco Webex	
47	IV-Yr & II-Sem	RFIC	25-05-2020, 08:30 to 10.30	Radio Architecture	Cisco Webex	
48	III-Yr & II-Sem	VLSI Design	25-05-2020, 08:30 to 9.30	Design Examples	Cisco Webex	
49	IV-Yr & II-Sem	RFIC	26-05-2020, 10:50 to 12.50	UMTS Radio architecture	Cisco Webex	
50	IV-Yr & II-Sem	RFIC	27-05-2020, 08:30 to 10.30	UMTS Radio architecture	Cisco Webex	
51	III-Yr & II-Sem	VLSI Design	27-05-2020, 11:50 to 12.50	Design Examples	Cisco Webex	
52	III-Yr & II-Sem	VLSI Design	28-05-2020, 11:50 to 12.50	BIST technique	Cisco Webex	
53	III-Yr & II-Sem	VLSI Design	29-05-2020, 08:30 to 9.30	test patterns	Cisco Webex	
54	III-Yr & II-Sem	VLSI Design	30-05-2020, 11:50 to 12.50	test pattern generation	Cisco Webex	