

Theory Online Classes Details

S.No.	Year/ Sem	Subject	Date of Lecture & Class timings	Topic	App Used	Link (if any Resources)
1	III-II(A)	VLSI Design	30-03-2020 & 10:50AM to 11:45AM	Gate level design	Cisco Webex	https://brightlights.gnomio.com/
2	III-II(C)	VLSI Design	30-03-2020 & 3:20PM to 4:15PM	Gate level design	Cisco Webex	https://brightlights.gnomio.com/
3	III-II(A)	VLSI Design	31-03-2020 & 1:20PM to 2:15PM	VLSI Design flow	Cisco Webex	https://brightlights.gnomio.com/
4	III-II(A)	VLSI Design	01-04-2020 & 8:40AM to 9:35AM	Logic gates & other complex gates	Cisco Webex	https://brightlights.gnomio.com/
5	III-II(A)	VLSI Design	03-04-2020 & 1:20PM to 2:15PM	Switch logic & Alternate gate circuits	Cisco Webex	https://brightlights.gnomio.com/
6	III-II(C)	VLSI Design	03-04-2020 & 3:20PM to 4:15PM	Logic gates & other complex gates	Cisco Webex	https://brightlights.gnomio.com/
7	III-II(A)	VLSI Design	04-04-2020 & 9:35AM to 10:30AM	Floor-planning	Cisco Webex	https://brightlights.gnomio.com/
8	III-II(C)	VLSI Design	04-04-2020 & 2:15PM to 3:10PM	Switch logic & Alternate gate circuits	Cisco Webex	https://brightlights.gnomio.com/
9	III-II(A)	VLSI Design	06-04-2020 & 10:50AM to 11:45AM	Placement	Cisco Webex	https://brightlights.gnomio.com/
10	III-II(C)	VLSI Design	06-04-2020 & 3:20PM to 4:15PM	Floor-planning	Cisco Webex	https://brightlights.gnomio.com/
11	III-II(A)	VLSI Design	07-04-2020 & 1:20PM to 2:15PM	Routing	Cisco Webex	https://brightlights.gnomio.com/
12	III-II(A)	VLSI Design	08-04-2020 & 8:40AM to 9:35AM	Global Routing	Cisco Webex	https://brightlights.gnomio.com/
13	III-II(C)	VLSI Design	13-04-2020 & 10:50AM to 11:45AM	Power delay estimation	Cisco Webex	https://brightlights.gnomio.com/
14	III-II(C)	VLSI Design	13-04-2020 & 3:20PM to 4:15PM	Routing	Cisco Webex	https://brightlights.gnomio.com/
15	III-II(A)	VLSI Design	15-04-2020 & 8:40AM to 9:35AM	Clock routing	Cisco Webex	https://brightlights.gnomio.com/
16	III-II(A)	VLSI Design	17-04-2020 & 1:20PM to 2:15PM	Power routing	Cisco Webex	https://brightlights.gnomio.com/
17	III-II(C)	VLSI Design	17-04-2020 & 3:20PM to 4:15PM	Clock routing	Cisco Webex	https://brightlights.gnomio.com/
18	III-II(A)	VLSI Design	20-04-2020 & 10:50AM to 11:45AM	Physical design overview	Cisco Webex	https://brightlights.gnomio.com/
19	III-II(C)	VLSI Design	20-04-2020 & 3:20PM to 4:15PM	Power routing	Cisco Webex	https://brightlights.gnomio.com/
20	III-II(A)	VLSI Design	21-04-2020 & 1:20PM to 2:15PM	Subsystem design	Cisco Webex	https://brightlights.gnomio.com/
21	III-II(A)	VLSI Design	22-04-2020 & 8:40AM to 9:35AM	Shifters	Cisco Webex	https://brightlights.gnomio.com/
22	III-II(A)	VLSI Design	24-04-2020 & 1:20PM to 2:15PM	Adders	Cisco Webex	https://brightlights.gnomio.com/
23	III-II(C)	VLSI Design	24-04-2020 & 3:20PM to 4:15PM	Subsystem design	Cisco Webex	https://brightlights.gnomio.com/
24	III-II(A)	VLSI Design	27-04-2020 & 10:50AM to 11:45AM	Full adder and 4 bit parallel adder	Cisco Webex	https://brightlights.gnomio.com/
25	III-II(C)	VLSI Design	27-04-2020 & 3:20PM to 4:15PM	Shifters	Cisco Webex	https://brightlights.gnomio.com/
26	III-II(A)	VLSI Design	28-04-2020 & 1:20PM to 2:15PM	Arithmetic Logic Unit	Cisco Webex	https://brightlights.gnomio.com/
27	III-II(A)	VLSI Design	29-04-2020 & 8:40AM to 9:35AM	Multipliers & Parity generators	Cisco Webex	https://brightlights.gnomio.com/
28	III-II(A)	VLSI Design	01-05-2020 & 1:20PM to 2:15PM	Comparators & Counters	Cisco Webex	https://brightlights.gnomio.com/
29	III-II(C)	VLSI Design	01-05-2020 & 3:20PM to 4:15PM	Adders	Cisco Webex	https://brightlights.gnomio.com/
30	III-II(C)	VLSI Design	04-05-2020 & 8:30AM to 9:30AM	Full adder and 4 bit parallel adder	Cisco Webex	https://brightlights.gnomio.com/
31	III-II(A)	VLSI Design	05-05-2020 & 8:30AM to 9:30AM	High Density Memory Elements	Cisco Webex	https://brightlights.gnomio.com/
32	III-II(A)	VLSI Design	06-05-2020 & 9:30AM to 10:30AM	VLSI Design Styles	Cisco Webex	https://brightlights.gnomio.com/

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33	III-II(C)	VLSI Design	07-05-2020 & 8:30AM to 9:30AM	Multipliers & Parity generators	Cisco Webex	https://brightlights.gnomio.com/
34	III-II(A)	VLSI Design	07-05-2020 & 11:50AM to 12:50PM	Standard Cells	Cisco Webex	https://brightlights.gnomio.com/
35	III-II(C)	VLSI Design	08-05-2020 & 11:50AM to 12:50PM	Comparators & Counters	Cisco Webex	https://brightlights.gnomio.com/
36	III-II(C)	VLSI Design	11-05-2020 & 8:30AM to 9:30AM	High Density Memory Elements	Cisco Webex	https://brightlights.gnomio.com/
37	III-II(A)	VLSI Design	12-05-2020 & 8:30AM to 9:30AM	FPGA	Cisco Webex	https://brightlights.gnomio.com/
38	III-II(C)	VLSI Design	12-05-2020 & 11:50AM to 12:50PM	VLSI Design Styles	Cisco Webex	https://brightlights.gnomio.com/
39	III-II(A)	VLSI Design	13-05-2020 & 9:30AM to 10:30AM	CPLD	Cisco Webex	https://brightlights.gnomio.com/
40	III-II(C)	VLSI Design	14-05-2020 & 8:30AM to 9:30AM	Standard Cells	Cisco Webex	https://brightlights.gnomio.com/
41	III-II(A)	VLSI Design	14-05-2020 & 11:50AM to 12:50PM	Design approach for IC's	Cisco Webex	https://brightlights.gnomio.com/
42	III-II(C)	VLSI Design	15-05-2020 & 11:50AM to 12:50PM	FPGA	Cisco Webex	https://brightlights.gnomio.com/
43	III-II(C)	VLSI Design	18-05-2020 & 8:30AM to 9:30AM	CPLD	Cisco Webex	https://brightlights.gnomio.com/
44	III-II(C)	VLSI Design	22-05-2020 & 11:50AM to 12:50PM	Design approach for IC's	Cisco Webex	https://brightlights.gnomio.com/
45	III-II(A)	VLSI Design	23-05-2020 & 8:30AM to 9:30AM	VHDL Synthesis	Cisco Webex	https://brightlights.gnomio.com/
46	III-II(A)	VLSI Design	26-05-2020 & 8:30AM to 9:30AM	Circuit Design flow and Synthesis	Cisco Webex	https://brightlights.gnomio.com/
47	III-II(A)	VLSI Design	27-05-2020 & 9:30AM to 10:30AM	Design capture and verification tools	Cisco Webex	https://brightlights.gnomio.com/
48	III-II(C)	VLSI Design	28-05-2020 & 8:30AM to 9:30AM	Circuit Design flow and Synthesis	Cisco Webex	https://brightlights.gnomio.com/
49	III-II(A)	VLSI Design	28-05-2020 & 11:50AM to 12:50PM	Test Generation	Cisco Webex	https://brightlights.gnomio.com/
50	III-II(A)	VLSI Design	29-05-2020 & 10:50AM to 11:50AM	Fault modeling and Simulation	Cisco Webex	https://brightlights.gnomio.com/
51	III-II(C)	VLSI Design	29-05-2020 & 11:50AM to 12:50PM	Design capture and verification tools	Cisco Webex	https://brightlights.gnomio.com/
52	III-II(A)	VLSI Design	30-05-2020 & 9:30AM to 10:30AM	Design for Testability	Cisco Webex	https://brightlights.gnomio.com/