

Theory Online Classes Details

S.No.	Year/ Sem	Subject	Date of Lecture & timings	Class	Topic	App Used
1	IV/II	Low Power VLSI Circuits & Systems	04-01-2020 & 10:50AM - 12:40PM		device features size scaling, architecture- level approaches	CISCO WEBEX
2	IV/II	Low Power VLSI Circuits & Systems	15-4-2020& 10:50AM - 12:40PM		system-level approaches, transmeta's Crusoe processor	CISCO WEBEX
3	IV/II	Low Power VLSI Circuits & Systems	20-04-2020 & 8:40AM - 10:30AM		bus encoding, clock gating, gated-clock FSMs	CISCO WEBEX
4	IV/II	Low Power VLSI Circuits & Systems	21-04-2020& 10:50AM - 12:40PM		FSM state encoding, FSM Partitioning	CISCO WEBEX
5	IV/II	Low Power VLSI Circuits & Systems	22-04-2020& 10:50AM - 12:40PM		operand isolation, pre computation, logic styles for low power	CISCO WEBEX
6	IV/II	Low Power VLSI Circuits & Systems	27-04-2020 & 8:40AM - 10:30AM		fabrication of multiple threshold voltages	CISCO WEBEX
7	IV/II	Low Power VLSI Circuits & Systems	28-04-2020& 10:50AM - 12:40PM		approaches for minimizing leakage power, Adiabatic Logic Circuits	CISCO WEBEX
8	IV/II	Low Power VLSI Circuits & Systems	29-04-2020& 10:50AM - 12:40PM		Battery-Driven System, CAD Tools for Low Power VLSI Circuits.	CISCO WEBEX
9	IV/II	Low Power VLSI Circuits & Systems	04-05-2020&10:50AM - 12:40PM		Revision on Short Circuit Power Dissipation	CISCO WEBEX
10	IV/II	Low Power VLSI Circuits & Systems	05-05-2020&08:40AM - 10:30AM		Revision on Switching Power Dissipation	CISCO WEBEX
11	IV/II	Low Power VLSI Circuits & Systems	06-05-2020&10:50AM - 12:40PM		Revision on Leakage Power Dissipation	CISCO WEBEX

Link (if any Resources)

